

# Model-Checking Over Multi-Valued Logics

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**Abstract.** Classical logic cannot be used to effectively reason about systems with *uncertainty* (lack of essential information) or *inconsistency* (contradictory information often occurring when information is gathered from multiple sources). In this paper we propose the use of *quasi-boolean* multi-valued logics for reasoning about such systems. We also give semantics to a multi-valued extension of CTL, describe an implementation of a symbolic multi-valued CTL model-checker called *Xchek*, and analyze its correctness and running time.

## 1 Introduction

In the last few years, *model checking* [9] has become established as one of the most effective automated techniques for analyzing correctness of software artifacts. Given a system and a property, a model checker builds the reachability graph (explicitly or symbolically) by exhaustively exploring the state-space of the system. Model-checking has been effectively applied to reasoning about correctness of hardware, communication protocols, software requirements and code, etc. A number of industrial model checkers have been developed, including SPIN [18], SMV [22], and Mur $\phi$  [12].

Despite their variety, existing model-checkers are typically limited to reasoning in classical logic. However, there are a number of problems in software engineering for which classical logic is insufficient. One of these is reasoning under *uncertainty*, or when essential information is not available. This can occur either when complete information is not known or cannot be obtained (e.g., during requirements analysis), or when this information has been removed (abstraction). Classical model-checkers typically deal with uncertainty by creating extra states, one for each value of the unknown variable and each feasible combination of values of known variables. However, this approach adds significant extra complexity to the analysis.

Classical reasoning is also insufficient for models that contain *inconsistency*. Inconsistency arises frequently in software engineering [15]. In requirements engineering, models are frequently inconsistent because they combine conflicting points of view. During design and implementation, inconsistency arises when integrating components developed by different people. Conventional reasoning systems cannot cope with inconsistency; the presence of a single contradiction results in trivialization — anything follows from  $A \wedge \neg A$ . Hence, faced with an inconsistent description and the need to perform automated reasoning, we must either discard information until consistency is achieved again, or adopt a non-classical logic. The problem with the former approach is that we may be forced to make premature decisions about which information to discard [19].

Although inconsistency in software engineering occurs very frequently, there have been relatively few attempts to develop automated reasoning tools for inconsistent models. Two notable exceptions are Hunter and Nuseibeh [20], who use a Quasi-Classical (QC) logic to reason about evolving specifications, and Menzies et al. [23], who use a paraconsistent form of abductive inference to reason about information from multiple points of view.

*Paraconsistent logics* are a promising alternative to classical reasoning — they permit some contradictions to be true, without the resulting trivialization of classical logic. The development of paraconsistent logics has been driven largely by the need for automated reasoning systems that do not give spurious answers if their databases become inconsistent. They are also of interest to mathematicians as a way of addressing the paradoxes in semantics and set theory. A number of different types of paraconsistent logic have been studied [24]. For example, relevance logics use an alternative form of entailment that requires a “relevant” connection between the antecedents and the consequents. Non-truth functional logics use a weaker form of negation so that proof rules such as disjunctive syllogism (i.e.,  $(A \vee B, \neg B) \vdash A$ ) fail. Multi-valued logics use additional truth values to represent different types of contradiction.

Multi-valued logics provide a solution to both reasoning under uncertainty and under inconsistency. For example, we can use “no information available” and “no agreement” as logic values. In fact, model-checkers based on three-valued and four-valued logics have already been studied. For example, [8] used a three-valued logic for interpreting results of model-checking with abstract interpretation, whereas [16, 17] used four-valued logics for reasoning about abstractions of detailed gate or switch-level designs of circuits.

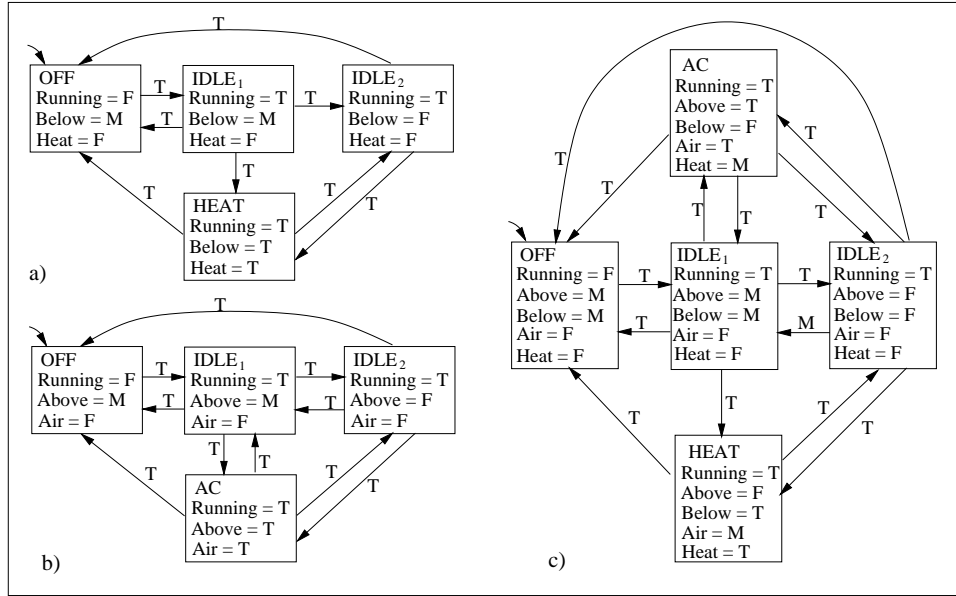
Different multi-valued logics are useful for different purposes. For example, we may wish to have several levels of uncertainty. We may wish to use different multi-valued logics to support different ways of merging information from multiple sources: keeping track of the origin of each piece of information, doing a majority vote, giving priority to one information source, etc. Thus, rather than restricting ourselves to any particular multi-valued logic, we are interested in extending the classical symbolic model-checking procedure to enable reasoning about arbitrary multi-valued logics, as long as conjunction, disjunction and negation of the logical values are specified.

This work is part of the  $\chi$ bel<sup>1</sup> (the Multi-Valued **B**elief **E**xploration **L**ogics) project, outlined in [14]. The description of the system together with the description of the desired multi-valued logic and the set of correctness criteria expressed in CTL become input to our model-checker, called  $\chi$ chek, which returns a value of the logic best characterizing the validity of the formula in each initial state.

The rest of this paper is organized as follows: Section 2 describes a simple thermostat system which is used as a running example throughout the paper. Section 3 gives background on CTL model-checking. Section 4 describes the types of logics that we can analyze and the ways to represent them. Section 5 describes the multi-valued transition structures and extends CTL to reasoning over them. Section 6 discusses the implementation of  $\chi$ chek, whereas Section 7 contains the analysis of its correctness and running

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<sup>1</sup> pronounced “Chibel”



**Fig. 1.** Models of the thermostat. (a) Heat only; (b) AC only; (c) combined model.

time. We conclude the paper with a summary of results and outline of future work in Section 8.

## 2 Example

Consider three models of the thermostat given in Figure 1. Figure 1(a) describes a very simple thermostat that can run a heater if the temperature falls below desired. The system has one indicator (*Below*), a switch to turn it off and on (*Running*) and a variable indicating whether the heater is running (*Heat*). The system starts in state *OFF*<sup>2</sup> and transitions into *IDLE*<sub>1</sub> when it is turned on, where it awaits the reading of the temperature indicator. Once the temperature is determined, the system transitions either into *IDLE*<sub>2</sub> or into *HEAT*. The value of the temperature indicator is unknown in states *OFF* and *IDLE*<sub>1</sub>. To model this, we could duplicate the states, assigning *Below* the value *T* in one copy and *F* in the other — the route typically taken by conventional model-checkers. Alternatively, we can model the system using the three-valued logic: *T*, *F* and *M* (Maybe), assigning *Below* the value *M*, as depicted in Figure 1(a)<sup>3</sup>.

We can ask this thermostat model a number of questions:

- Prop. 1. Can the system transition into *IDLE*<sub>1</sub> from everywhere?
- Prop. 2. Can the heater be turned on when the temperature becomes below desired?
- Prop. 3. Can the system be turned off in every computation?

<sup>2</sup> Throughout this paper state labels are capitalized. Thus, *HEAT* is a state and *Heat* is a variable name.

<sup>3</sup> Each state in this and the other two systems in Figure 1 contains a self-loop with the value *T* which we omitted to avoid clutter.

Figure 1(b) describes another aspect of the thermostat system – running the air conditioner. The behavior of this system is similar to that of the heater, with one difference: this system handles the failure of the temperature indicator. If the temperature reading cannot be obtained in states  $AC$  or  $IDLE_1$ , the system transitions into state  $IDLE_1$ .

Finally, Figure 1(c) contains a merged model, describing the behavior of the thermostat that can run both the heater and the air conditioner. In this merge, we used the same three-valued logic, for simplicity. When the individual descriptions agree that the value of a variable or transition is  $T(F)$ , it is mapped into  $T(F)$  in the combined model; all other values are mapped into  $M$ . During the merge, we used the simple invariants describing the behavior of the environment ( $Below \rightarrow \neg Above$ ,  $Above \rightarrow \neg Below$ ). Thus, the value of  $Below$  in state  $AC$  is inferred to be  $F$ . Note that the individual descriptions disagree on some states and transitions. For example, they disagree on a transition between  $IDLE_2$  and  $IDLE_1$ ; thus it receives the value  $M$ . Also, it is possible that the heater is on while the air conditioner is running.

Further details on the merge procedure are outside the scope of this paper, except to note that we could have chosen any of a number of different multi-valued logics to handle different combinations of values in the individual models. For example, we could have used a 9-valued logic where each value is a tuple formed from the values of the two individual models.

We can ask the combined model a number of questions that cannot be answered by either individual model, e.g.

Prop. 4. Is heat on only if air conditioning is off?

Prop. 5. Can heat be on when the temperature is above desired?

### 3 CTL Model-Checking

CTL model-checking is an automatic technique for verifying properties expressed in a propositional branching-time temporal logic called *Computational Tree Logic* (CTL) [9]. The system is defined by a Kripke structure, and properties are evaluated on a tree of infinite computations produced by the model of the system. The standard notation  $M, s \models P$  indicates that a formula  $P$  holds in a state  $s$  of a model  $M$ . If a formula holds in the initial state, it is considered to hold in the model.

A Kripke structure consists of a set of states  $S$ , a transition relation  $R \subseteq S \times S$ , an initial state  $s_0 \in S$ , a set of atomic propositions  $A$ , and a labeling function  $L : S \rightarrow 2^A$ .  $R$  must be total, i.e.,  $\forall s \in S, \exists t \in S, \text{ s.t. } (s, t) \in R$ . If a state  $s_n$  has no successors, we add a self-loop to it, so that  $(s_n, s_n) \in R$ . For each  $s \in S$ , the labeling function provides a list of atomic propositions which are *True* in this state.

CTL is defined as follows:

1. Every atomic proposition  $a \in A$  is a CTL formula.
2. If  $\varphi$  and  $\psi$  are CTL formulas, then so are  $\neg\varphi$ ,  $\varphi \wedge \psi$ ,  $\varphi \vee \psi$ ,  $EX\varphi$ ,  $AX\varphi$ ,  $EF\varphi$ ,  $AF\varphi$ ,  $E[\varphi U \psi]$ ,  $A[\varphi U \psi]$ .

The logic connectives  $\neg$ ,  $\wedge$  and  $\vee$  have their usual meanings. The existential (universal) quantifier  $E$  ( $A$ ) is used to quantify over paths. The operator  $X$  means “at the next step”,

$F$  represents “sometime in the future”, and  $U$  is “until”. Therefore,  $EX\varphi$  ( $AX\varphi$ ) means that  $\varphi$  holds in some (every) immediate successor of the current program state;  $EF\varphi$  ( $AF\varphi$ ) means that  $\varphi$  holds in the future along some (every) path emanating from the current state;  $E[\varphi U \psi]$  ( $A[\varphi U \psi]$ ) means that for some (every) computation path starting from the current state,  $\varphi$  continuously holds until  $\psi$  becomes true. Finally, we use  $EG(\varphi)$  and  $AG(\varphi)$  to represent the property that  $\varphi$  holds at every state for some (every) path emanating from  $s_0$ . Formally,

$$\begin{aligned}
M, s_0 &\models a \text{ iff } a \in L(s_0) \\
M, s_0 &\models \neg\varphi \text{ iff } M, s_0 \not\models \varphi \\
M, s_0 &\models \varphi \wedge \psi \text{ iff } M, s_0 \models \varphi \wedge M, s_0 \models \psi \\
M, s_0 &\models \varphi \vee \psi \text{ iff } M, s_0 \models \varphi \vee M, s_0 \models \psi \\
M, s_0 &\models EX\varphi \text{ iff } \exists t \in S, (s_0, t) \in R \wedge M, t \models \varphi \\
M, s_0 &\models AX\varphi \text{ iff } \forall t \in S, (s_0, t) \in R \rightarrow M, t \models \varphi \\
M, s_0 &\models E[\varphi U \psi] \text{ iff there exists some path } s_0, s_1, \dots, \text{ s.t.} \\
&\quad \exists i, i \geq 0 \wedge M, s_i \models \psi \wedge \\
&\quad \forall j, 0 \leq j < i \rightarrow M, s_j \models \varphi \\
M, s_0 &\models A[\varphi U \psi] \text{ iff for every path } s_0, s_1, \dots, \\
&\quad \exists i, i \geq 0 \wedge M, s_i \models \psi \wedge \\
&\quad \forall j, 0 \leq j < i \rightarrow M, s_j \models \varphi.
\end{aligned}$$

where the remaining operators are defined as follows:

$$\begin{aligned}
AF(\varphi) &\equiv A[\top U \varphi] && \text{(def. of } AF) \\
EF(\varphi) &\equiv E[\top U \varphi] && \text{(def. of } EF) \\
AG(\varphi) &\equiv \neg EF(\neg\varphi) && \text{(def. of } AG) \\
EG(\varphi) &\equiv \neg AF(\neg\varphi) && \text{(def. of } EG)
\end{aligned}$$

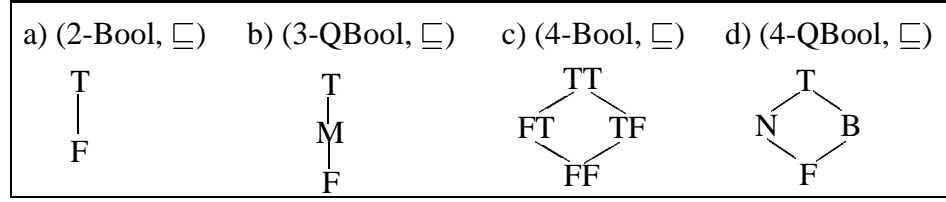
Definitions of  $AF$  and  $EF$  indicate that we are using a “strong until”, that is,  $E[\varphi U \psi]$  and  $A[\varphi U \psi]$  are true only if  $\psi$  eventually occurs.

## 4 Specifying the Logic

Since our model checker works for different multi-valued logics, we need a way to specify the particular logic we wish to use. We can specify a logic by giving its inference rules or by defining conjunction, disjunction and negation operations on the elements of the logic. Since our goal is model-checking as opposed to theorem proving, we chose the latter approach. Further, the logic should be as close to classical as possible; in particular, the defined operations should be idempotent, commutative, etc. Such properties can be easily guaranteed if we ensure that the values of the logic form a lattice. Indeed, lattices are a natural way to specify our logics. In this section we give a brief introduction to lattice theory and describe the types of lattices used by our model-checker.

### 4.1 Lattice Theory

We introduce lattice theory here following the presentation in [2].



**Fig. 2.** Examples of logic lattices: (a) a two-valued lattice representing classical logic; (b) a three-valued lattice reflecting uncertainty; (c) a four-valued boolean lattice, a product of two (2-Bool,  $\sqsubseteq$ ) lattices; (d) a four-valued quasi-boolean lattice.

**Definition 1** Lattice is a partial order  $(\mathcal{L}, \sqsubseteq)$  for which a unique least upper bound and greatest lower bound, denoted  $a \sqcup b$  and  $a \sqcap b$  exist for each pair of elements  $(a, b)$ .

The following are the properties of lattices:

$$\begin{aligned}
 a \sqcup a &= a & (\text{idempotence}) \\
 a \sqcap a &= a \\
 a \sqcup b &= b \sqcup a & (\text{commutativity}) \\
 a \sqcap b &= b \sqcap a \\
 a \sqcup (b \sqcap c) &= (a \sqcup b) \sqcup c & (\text{associativity}) \\
 a \sqcap (b \sqcup c) &= (a \sqcap b) \sqcup c \\
 a \sqcup (a \sqcap b) &= a & (\text{absorption}) \\
 a \sqcap (a \sqcup b) &= a \\
 a \sqsubseteq a' \wedge b \sqsubseteq b' &\Rightarrow a \sqcap b \sqsubseteq a' \sqcap b' & (\text{monotonicity}) \\
 a \sqsubseteq a' \wedge b \sqsubseteq b' &\Rightarrow a \sqcup b \sqsubseteq a' \sqcup b'
 \end{aligned}$$

$a \sqcap b$  and  $a \sqcup b$  are referred to as *meet* and *join*, representing for us conjunction and disjunction operations, respectively. Figure 2 gives examples of a few logic lattices. Our partial order operation  $a \sqsubseteq b$  means that “ $b$  is more true than  $a$ ”.

**Definition 2** A lattice is distributive if

$$\begin{aligned}
 a \sqcup (b \sqcap c) &= (a \sqcup b) \sqcap (a \sqcup c) & (\text{distributivity}) \\
 a \sqcap (b \sqcup c) &= (a \sqcap b) \sqcup (a \sqcap c)
 \end{aligned}$$

All lattices in Figure 2 are distributive.

**Definition 3** A lattice is complete if the least upper bound and the greatest lower bound for each subset of elements of the lattice is an element of the lattice. Every complete lattice has a top and bottom.

$$\begin{aligned}
 \perp &= \sqcap \mathcal{L} & (\perp \text{ characterization}) \\
 \top &= \sqcup \mathcal{L} & (\top \text{ characterization})
 \end{aligned}$$

In this paper we use  $\top$  to indicate  $\top$  of the lattice, and  $\perp$  to indicate its  $\perp$ , although in principle  $\top$  and  $\perp$  might be labelled differently.

Finite lattices are complete by definition. Thus, all lattices representing finite-valued logics are complete.

**Definition 4** A complete distributive lattice is called a complete Boolean lattice if every element  $a \in \mathcal{L}$  has a unique complement  $\neg a \in \mathcal{L}$  satisfying the following conditions:

$$\begin{array}{llll} \neg\neg a = a & (\neg \text{ involution}) & a \sqsubseteq b \Leftrightarrow \neg a \sqsupseteq \neg b & (\neg \text{ antimonotonic}) \\ \neg(a \sqcap b) = \neg a \sqcup \neg b & (\text{de Morgan}) & a \sqcap \neg a = \perp & (\neg \text{ contradiction}) \\ \neg(a \sqcup b) = \neg a \sqcap \neg b & (\text{de Morgan}) & a \sqcup \neg a = \top & (\neg \text{ exhaustiveness}) \end{array}$$

In fact,  $\neg$  involution, de Morgan and antimonotonic laws follow from  $\neg$  contradiction and  $\neg$  exhaustiveness.

**Definition 5** A product of two lattices  $(\mathcal{L}_1, \sqsubseteq)$ ,  $(\mathcal{L}_2, \sqsubseteq)$  is a lattice  $(\mathcal{L}_1 \times \mathcal{L}_2)$ , with the ordering  $\sqsubseteq$  holding between two pairs iff it holds for each component separately, i.e.

$$(a, b) \sqsubseteq (a', b') \Leftrightarrow a \sqsubseteq a' \wedge b \sqsubseteq b'$$

Bottom, top, complement, meet and join in the product lattice are component-wise extensions of the corresponding operations of the component lattices. Product of two lattices preserves their distributivity, completeness and boolean properties. For example, out of the four lattices in Figure 2, only (2-Bool,  $\sqsubseteq$ ) and (4-Bool,  $\sqsubseteq$ ) are boolean. The former is boolean because  $\neg T = F$ ,  $\neg F = T$ . The latter is a product of two (2-Bool,  $\sqsubseteq$ ) lattices and thus is complete, distributive and boolean. The lattice (3-QBool,  $\sqsubseteq$ ) is not boolean because  $\neg M = M$ , and  $M \sqcap \neg M \neq \perp$ .

## 4.2 Quasi-Boolean Lattices

**Definition 6** A distributive lattice  $(\mathcal{L}, \sqsubseteq)$  is quasi-boolean [4] (also called de Morgan [13]) if there exists a unary operator  $\neg$  defined for it, with the following properties ( $a, b$  are elements of  $(\mathcal{L}, \sqsubseteq)$ ):

$$\begin{array}{llll} \neg(a \sqcap b) = \neg a \sqcup \neg b & (\text{de Morgan}) & \neg\neg a = a & (\neg \text{ involution}) \\ \neg(a \sqcup b) = \neg a \sqcap \neg b & & a \sqsubseteq b \Leftrightarrow \neg a \sqsupseteq \neg b & (\neg \text{ antimonotonic}) \end{array}$$

Thus,  $\neg a$  is a quasi-complement of  $a$ .

Therefore, all boolean lattices are also quasi-boolean, whereas the converse is not true. Logics represented by quasi-boolean lattices will be referred to as *quasi-boolean logics*.

**Theorem 1** A product of two quasi-boolean lattices is quasi-boolean

**Proof:**

Refer to the Appendix for proof of this and other theorems of this paper.  $\square$

For example, the lattice (3-QBool,  $\sqsubseteq$ ), first defined in [21], and all its products are quasi-boolean. We refer to  $n$ -valued boolean lattices as  $(n\text{-Bool}, \sqsubseteq)$  and to quasi-boolean lattices as  $(n\text{-QBool}, \sqsubseteq)$ . (4-QBool,  $\sqsubseteq$ ) is a lattice for a logic proposed by Belnap for reasoning about inconsistent databases [3, 1]. This lattice is quasi-boolean ( $\neg N = N$ ;  $\neg B = B$ ) and thus not isomorphic to (4-Bool,  $\sqsubseteq$ ).

In the rest of this paper we assume that the negation operator given for our logic makes the lattice quasi-boolean. What do quasi-boolean lattices look like? Below we

define lattices which are (geometrically) horizontally-symmetric and show that, with negation defined by the horizontal symmetry, this is a sufficient condition for quasi-booleanness. We define:

**Definition 7** A lattice  $(\mathcal{L}, \sqsubseteq)$  is horizontally-symmetric if there exists a bijective function  $H$  such that for every pair  $a, b \in \mathcal{L}$ ,

$$\begin{aligned} a \sqsubseteq b &\Leftrightarrow H(a) \supseteq H(b) \quad (\text{order} - \text{embedding}) \\ H(H(a)) &= a \quad (H \text{ involution}) \end{aligned}$$

**Theorem 2** Let  $(\mathcal{L}, \sqsubseteq)$  be a horizontally-symmetric lattice. Then the following hold for any two elements  $a, b \in \mathcal{L}$ :

$$\begin{aligned} H(a \sqcap b) &= H(a) \sqcup H(b) \\ H(a \sqcup b) &= H(a) \sqcap H(b) \end{aligned}$$

Thus, horizontal symmetry is a sufficient condition for the corresponding lattice to be quasi-boolean, with  $\neg a = H(a)$  for each element of the lattice, since it guarantees antimonotonicity and involution by definition, and de Morgan laws via Theorem 2.

## 5 Multi-Valued CTL Model-Checking

In this section we extend the notion of boolean model-checking described in Section 3 by defining multi-valued Kripke structures and multi-valued CTL.

### 5.1 Defining the Model

A state machine  $M$  is a *multi-valued Kripke* ( $X$ Kripke) structure if  $M = (S, S_0, R, I, A, L)$ , where

- $L$  is a quasi-boolean logic represented by a lattice  $(\mathcal{L}, \sqsubseteq)$ .
- $A$  is a (finite) set of atomic propositions, otherwise referred to as variables (e.g. Running, Below, Heat in Figure 1(a)). For simplicity, we assume that all variables are of the same type.
- $S$  is a (finite) set of states; each state is identified by a unique (within  $M$ ) label  $s$ .  $S_0 \subseteq S$  is the non-empty set of initial states.
- Each transition  $(s, t)$  in  $M$  has a logical value in  $\mathcal{L}$ , referred to as  $\langle s, t \rangle^M$ , or, when  $M$  is clear from the context, simply as  $\langle s, t \rangle$ . Then,  $R : \mathcal{L} \rightarrow 2^{S \times S}$  is the labeling function mapping logical value  $v \in \mathcal{L}$  into a set of transitions  $\{(s, t)\}$  where the value of each  $(s, t)$  is  $v$ , i.e.

$$\forall v \in \mathcal{L}, R(v) = \{(s, t) \mid s \in S \wedge t \in S \wedge \langle s, t \rangle = v\}$$

We also ensure that there is at least one non-false transition out of each state, extending the classical notion of Kripke structures. Formally,

$$\forall s \in S, \exists t \in S, \exists v \in \mathcal{L} \text{ s.t. } v \neq \perp \wedge (s, t) \in R(v)$$



$\neg AX\varphi = EX(\neg\varphi)$	(negation of “next”)
$A[\perp U\varphi] = E[\perp U\varphi] = \varphi$	( $\perp$ “until”)
$A[\varphi U\psi] = \psi \vee (\varphi \wedge AXA[\varphi U\psi] \wedge EXA[\varphi U\psi])$	( $AU$ fixpoint)
$E[\varphi U\psi] = \psi \vee (\varphi \wedge EXE[\varphi U\psi])$	( $EU$ fixpoint)

**Fig. 3.** Properties of CTL operators.

To avoid clutter, we follow the convention of finite-state machines of not drawing F transitions. Thus, in Figure 1(a), transition between  $IDLE_2$  and  $IDLE_1$  is F, whereas in Figure 1(c) this transition is M. We refer to a value that a variable (an atomic proposition)  $a$  takes in state  $s$  as  $\langle a \rangle_s^M$ , or, when  $M$  is clear from context, simply as  $\langle a \rangle_s$ .

- $I : A \times \mathcal{L} \rightarrow 2^S$  is a labeling function that maps each atomic proposition (variable)  $a$  and each logical value  $v$  to a set of states where the value of  $a$  is  $v$ , i.e.,

$$\forall v \in \mathcal{L}, \forall a \in A, I(a, v) = \{s \mid s \in S \wedge \langle a \rangle_s^M = v\}$$

Thus, the expression  $(\langle \varphi \rangle_s = T) \vee (\langle s, t \rangle = F)$  means that we check whether the value of  $\varphi$  in  $s$  is T, or whether the value of the transition  $(s, t)$  is F. Alternatively, and equivalently, we can think of a state  $s$  as a vector of variables  $A$  and their values in  $\mathcal{L}$  together with a unique label  $s$ .

## 5.2 Multi-Valued CTL

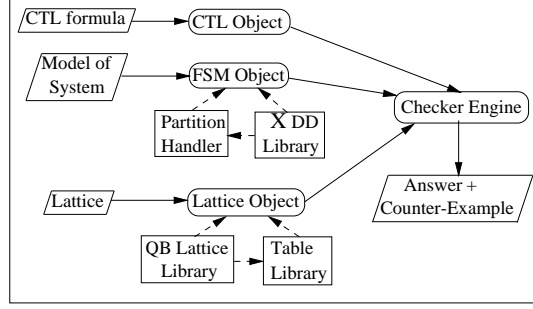
Here we give semantics of CTL operators on a  $\chi$ Kripke structure  $M$  over a quasi-boolean logic  $L$ . We will refer to this language as *multi-valued CTL*, or  $\chi$ CTL.  $L$  is described by a finite, quasi-boolean lattice  $(\mathcal{L}, \sqsubseteq)$ , and thus the conjunction  $\sqcap$ , disjunction  $\sqcup$  and negation  $\neg$  operations are available. In extending the CTL operators, we want to ensure that the expected CTL properties, given in Figure 3, are still preserved. Note that the  $AU$  fixpoint is somewhat unusual because it includes an additional conjunct,  $EXA[fUg]$ . The reason for this term is to preserve a “strong until” semantics for states that have no outgoing T transitions. This term was introduced by [6] for reasoning about non-Kripke structures.

We start defining  $\chi$ CTL by giving the semantics of propositional operators. Here,  $s$  is a state and  $v \in \mathcal{L}$  is a logic value:

$$\begin{aligned} \langle a \rangle_s &= v \text{ iff } a \in I(v) \\ \langle \neg\varphi \rangle_s &= v \text{ iff } \langle \varphi \rangle_s = \neg v \\ \langle \varphi \wedge \psi \rangle_s &= v \text{ iff } (\langle \varphi \rangle_s \sqcap \langle \psi \rangle_s) = v \\ \langle \varphi \vee \psi \rangle_s &= v \text{ iff } (\langle \varphi \rangle_s \sqcup \langle \psi \rangle_s) = v \end{aligned}$$

We proceed by defining  $EX$  and  $AX$  operators. Recall from Section 3 that these operators were defined using existential and universal quantification over next states. We extend the notion of quantification for multi-valued reasoning by using conjunction and disjunction operators. This treatment of quantification is standard [3, 25]. The semantics of  $EX$  and  $AX$  operators is given below:

$$\begin{aligned} \langle EX\varphi \rangle_s &= v \text{ iff } (\bigvee_{t \in \text{succ}(s)} (\langle s, t \rangle \wedge \langle \varphi \rangle_t)) = v \\ \langle AX\varphi \rangle_s &= v \text{ iff } (\bigwedge_{t \in \text{succ}(s)} (\langle s, t \rangle \rightarrow \langle \varphi \rangle_t)) = v \end{aligned}$$



**Fig. 4.** Architecture of  $\chi$ chek.

**Theorem 3** *Definitions of  $\langle EX\varphi \rangle_s$  and  $\langle AX\varphi \rangle_s$  preserve the negation of “next” property, i.e.*

$$\forall s \in S, \neg \langle AX\varphi \rangle_s = \langle EX\neg\varphi \rangle_s$$

Finally, we define  $AU$  and  $EU$  operators using the  $AU$  and  $EU$  fixpoint properties and the above definition of  $AX$  and  $EX$ :

$$\begin{aligned} \langle E[\varphi U \psi] \rangle_s &= v \text{ iff } ((\psi)_s \vee ((\varphi)_s \wedge \bigvee_{t \in \text{succ}(s)} ((s, t) \wedge \langle E[\varphi U \psi] \rangle_t))) = v \\ \langle A[\varphi U \psi] \rangle_s &= v \text{ iff } ((\psi)_s \vee ((\varphi)_s \wedge \bigwedge_{t \in \text{succ}(s)} ((s, t) \rightarrow \langle A[\varphi U \psi] \rangle_t) \\ &\quad \wedge \bigvee_{t \in \text{succ}(s)} ((s, t) \wedge \langle A[\varphi U \psi] \rangle_t))) = v \end{aligned}$$

The remaining CTL operators,  $AF(\varphi)$ ,  $EF(\varphi)$ ,  $AG(\varphi)$ ,  $EG(\varphi)$  are the abbreviations for  $A[\top U \varphi]$ ,  $E[\top U \varphi]$ ,  $\neg EF(\neg\varphi)$ ,  $\neg AF(\neg\varphi)$ , respectively.

## 6 $\chi$ chek: A Multi-Valued Model-Checker

In this section we describe our implementation of a multi-valued CTL model-checker. This symbolic model-checker, called  $\chi$ chek, is written in Java, and its architecture is depicted in Figure 4. The checking engine receives the  $\chi$ CTL formulas to verify, the model of the system represented as an  $\chi$ Kripke structure, and a lattice of logic values, and checks whether the specified property holds, returning an answer (one of the values of the passed lattice) and a counter-example, if appropriate.  $\chi$ chek uses four supplementary libraries:  $\chi$ DDs (a multi-valued extension of binary decision diagrams [5], described in [10]), a library for handling quasi-boolean lattices, a partition handler and a table inverter. The functionality of the latter two libraries is described below.

### 6.1 Table Library

The Table library contains several tables, indexed by the elements of the lattice, that give quick access to a variety of operations on lattice elements. In order to enable this indexing, we define  $\text{Ord} : \mathcal{L} \rightarrow \mathbf{N}$  — a total order on the elements of our lattice  $(\mathcal{L}, \sqsubseteq)$ .  $\text{Ord}()$  is a bijection, mapping each element  $v \in \mathcal{L}$  onto the set  $\{1 \dots |\mathcal{L}|\}$ . For example, we can order the elements of the lattice  $(3\text{-QBool}, \sqsubseteq)$  as follows:

$$\text{Ord}(T) = 1 \quad \text{Ord}(M) = 2 \quad \text{Ord}(F) = 3$$

This ordering is referred to as  $T < M < F$ .

Using  $\text{Ord}()$  and the primitive lattice operations, we compute *inverted tables*: given a value, these tables give pairs of elements yielding this value when the corresponding operation is performed on them. Three inverted tables,  $\text{InvTable}_\wedge$ ,  $\text{InvTable}_\rightarrow$  and  $\text{InvTable}_\vee$  are computed, one for each operator. For a table  $T$  and a value  $v$ , we use notation  $T_v$  to indicate an element associated with value  $v$ .

$\text{InvTable}_\wedge$  is defined as

$$\forall v \in \mathcal{L}, \text{InvTable}_{\wedge, v} = \{(v_1, v_2) \mid v_1, v_2 \in \mathcal{L} \wedge v_1 \sqcap v_2 = v\}$$

For example, for the lattice  $(3\text{-QBool}, \sqsubseteq)$ ,  $\text{InvTable}_{\wedge, M} = \{(M, M), (T, M), (M, T)\}$ .  $\text{InvTable}_\vee$  and  $\text{InvTable}_\rightarrow$  are similar, where  $\text{InvTable}_\rightarrow$  uses the identity

$$a \rightarrow b \equiv \neg a \sqcup b \quad (\text{definition of } \rightarrow)$$

Afterwards, we build generalized versions of the inverted tables for conjunction and disjunction over more than two operands. We call them  $\text{BigOPTable}_\wedge$  and  $\text{BigOPTable}_\vee$ . Given a logic value  $v$ ,  $\text{BigOPTable}_{\wedge, v}$  gives sets of logic values, where the corresponding operation over the elements of the set yields  $v$ . For example, for the lattice  $(3\text{-QBool}, \sqsubseteq)$ ,  $\text{BigOPTable}_{\wedge, M}$  is  $\{\{M\}, \{M, T\}\}$ .  $\text{BigOPTable}_\wedge$  is defined as

$$\forall v \in \mathcal{L}, \text{BigOPTable}_{\wedge, v} = \{V \mid V \in \mathcal{P}(\mathcal{L}) \wedge \bigwedge V = v\}$$

$\text{BigOPTable}_\vee$  is defined similarly.

## 6.2 The Partition Handler

Central to the design of Xchek is the notion of a *partition*. A partition separates the states of the model into subsets corresponding to the different values of the logic for a proposition  $\varphi$ . More formally, a partition  $[\varphi]^M$  for a property  $\varphi$  and a machine  $M$  is a tuple of sets, ordered via  $\text{Ord}()$ , such that the  $i$ th element of the tuple is a set of states where  $\varphi$  has the value  $\text{Ord}^{-1}(i)$  in  $M$ . When the choice of  $M$  is clear, we omit it from notation, referring to a partition simply as  $[\varphi]$ . The sets of states in a partition are mutually disjoint; thus, each proposition partitions the state space. For a value  $v \in \mathcal{L}$ , we write  $[\varphi]_v$  to indicate the set of states associated with  $v$ . For an example in Figure 1(a) and ordering  $T < M < F$ ,  $[\text{Below}] = (\{\text{Heat}\}, \{\text{Off}, \text{IDLE}_1\}, \{\text{IDLE}_2\})$ . Thus,

$$\forall s \in S, \forall v \in \mathcal{L}, (s \in [\varphi]_v \Leftrightarrow \langle \varphi \rangle_s = v)$$

In fact, operations  $\langle \rangle$  and  $\sqcup$  are Galois-connected. We also refer to  $\text{PartitionType}$  as a type  $(s_i \mid 1 \leq i \leq |\mathcal{L}| \rightarrow s_i \in 2^S)$ . Note that  $[\varphi]$  is of type  $\text{PartitionType}$ ; however, an element of  $\text{PartitionType}$  does not require its sets to be mutually-disjoint, although the union of the sets should still cover the entire state-space.

Further, we define a *predecessor function*  $\text{pred}()$  which receives a partition  $[\varphi]$  and an operator  $op \in \{\wedge, \rightarrow\}$  and returns a  $\text{PartitionType}$ . A state  $s$  is associated with value  $v_1$  in  $\text{pred}([\varphi], \wedge)$  iff  $s$  has a successor state  $t$  where  $\varphi$  has value  $v_2$ , and  $\langle s, t \rangle op v_2 = v_1$ . The function is given in Figure 5. Note that the result of  $\text{pred}$  is not necessarily a partition: a state can be in more than one set. For the lattice  $(3\text{-QBool}, \sqsubseteq)$ , its ordering  $T < M$

```

function pred( $[\varphi], op$ ){
  foreach  $v \in \mathcal{L}$ 
     $[\text{pred}]_v := \{s \mid \exists t \in S, \exists (v_1, v_2) \in \text{InvTable}_{op, v, s, t},$ 
       $((s, t) \in R(v_1)) \wedge (\langle \varphi \rangle_t = v_2)\}$ 
    return pred
}
function doOP( $[\varphi], op, [\psi]$ ) {
  foreach  $v \in \mathcal{L}$ 
     $[\text{result}]_v := \{\varphi(a) \cap \psi(b) \mid (a, b) \in \text{InvTable}_{op, v}\}$ 
  return result
}
function doBigOP( $op, [\varphi]$ ) {
  foreach  $v \in \mathcal{L}$ 
     $[\text{result}]_v := \emptyset$ 
    foreach  $V \in \text{BigOPTable}_{op, v}$ 
       $[\text{result}]_v := \{\bigcap_{v_i \in V} [\varphi]_{v_i} - \bigcup_{v_i \in (\mathcal{L} - V)} [\varphi]_{v_i}\} \cup [\text{result}]_v$ 
    return result
}
function QUntil(quantifier,  $[\varphi], [\psi]$ ) {
   $QU_0 = [\psi]$ 
  repeat
     $\text{EXTerm}_{i+1} := \text{doBigOP}(\vee, \text{pred}(QU_i, \wedge))$ 
    if (quantifier is A)
       $\text{AXTerm}_{i+1} := \text{doBigOP}(\wedge, \text{pred}(QU_i, \rightarrow))$ 
    else
       $\text{AXTerm}_{i+1} := [\varphi]$ 
    foreach  $v_1, v_2, v_3, v_4 \in \mathcal{L}$ 
       $\text{toMove} := [\varphi]_{v_1} \cap [\psi]_{v_2} \cap [\text{AXTerm}_{i+1}]_{v_3} \cap [\text{EXTerm}_{i+1}]_{v_4}$ 
       $\text{dest} := (v_1 \sqcap v_3 \sqcap v_4) \sqcup v_2$ 
      move all the states in toMove to  $[QU_{i+1}]_{\text{dest}}$ 
    until  $QU_{i+1} = QU_i$ 
  return  $QU_n$ 
}
procedure Check( $p$ ){
  Case
     $p \in A$ : return  $[p]$  where  $\forall v \in \mathcal{L}, [p]_v := I(p, v)$ 
     $p = \neg \varphi$ : return  $[p]$  where  $\forall v \in \mathcal{L}, [p]_v := [\varphi]_{\neg v}$ 
     $p = \varphi \wedge \psi$ : return doOP( $[\varphi], \wedge, [\psi]$ )
     $p = \varphi \vee \psi$ : return doOP( $[\varphi], \vee, [\psi]$ )
     $p = EX\varphi$ : return doBigOP( $\vee, \text{pred}([\varphi], \wedge)$ )
     $p = AX\varphi$ : return doBigOP( $\wedge, \text{pred}([\varphi], \rightarrow)$ )
     $p = E[\varphi U \psi]$ : return QUntil(E,  $[\varphi], [\psi]$ )
     $p = A[\varphi U \psi]$ : return QUntil(A,  $[\varphi], [\psi]$ )
}

```

Fig. 5. Algorithm for  $\chi$ chek.

$< F$  and the model in Figure 1(c),  $\text{pred}([\text{Running}], \wedge)$  returns  $(\{\text{IDLE}_1, \text{IDLE}_2, \text{Heat}\}, \{\text{IDLE}_1, \text{IDLE}_2\}, \{\text{IDLE}_1, \text{IDLE}_2, \text{AC}, \text{HEAT}, \text{OFF}\})$ .

We further define functions  $\text{doOp}()$  and  $\text{doBigOp}()$ , described in Figure 5. These functions evaluate the expression using the appropriate table ( $\text{InvTable}_{op}$  or  $\text{BigOPTable}_{op}$ ). Given partitions  $[\varphi]$  and  $[\psi]$ ,  $\text{doOp}$  returns a partition for  $\varphi \text{ op } \psi$ . For the lattice  $(3\text{-QBool}, \sqsubseteq)$  and the model in Figure 1(c),  $[\text{doOp}([\text{Above}], \vee, [\text{Below}])]_{\text{T}}$  returns a set of states in which  $\text{Above} \vee \text{Below}$  is T, namely,  $\{\text{AC}, \text{HEAT}\}$ .

$\text{doBigOp}(op, [\varphi])$  computes BigOP over a collection of states. Here,  $[\varphi]$  is required to be of  $\text{PartitionType}$  but does not need to be a partition, that is, the union of sets of states associated with each value of  $[\varphi]$  includes the entire state space, but these sets are not necessarily pairwise disjoint.  $\text{BigOPTable}_{op, v}$  includes sets of states such that the operation  $op$  performed on them yields  $v$ . Thus, for each  $V$  in  $\text{BigOPTable}_{op, v}$ , we compute the intersection of states for which  $\varphi$  has a value in  $V$  and subtract the union of states in which  $\varphi$  does not have a value in  $V$ .  $[\text{result}]_v$  includes the union of all states computed via the above process for all  $V$  in  $\text{BigOPTable}_{op, v}$ . For the model in Figure 1(c),  $[\text{doBigOp}(\vee, [\text{Heat}])]_{\text{T}}$  returns a set of states  $S'$  for which  $\bigvee_{s \in S'} [\text{Heat}]_s = \text{T}$ , namely,  $\{\text{IDLE}_1, \text{IDLE}_2, \text{HEAT}\}$ . Note that if  $[\varphi]$  represents a partition,  $\text{doBigOp}(op, [\varphi])$  simply returns a partition  $[\psi]$  where  $[\psi]_v = [\varphi]_v$  for  $v \in \mathcal{L}$ .

### 6.3 Algorithm of Xchek

The high-level algorithm, inspired by Bultan's symbolic model checker for infinite-state systems [6, 7] and an abstract model-checker of [8], is given in procedure  $\text{Check}()$  in Figure 5. The algorithm recursively goes through the structure of the property under the analysis, associating each subproperty  $\varphi$  with a partition  $[\varphi]$ . In fact,  $\text{Check}$  always returns partitions on the state-space (see Theorem 5). For the example in Figure 1(c) and the lattice ordering  $\text{T} < \text{M} < \text{F}$ ,

$$\begin{aligned} \text{Check}(\neg \text{Running}) &= (\{\text{OFF}\}, \{\}, \{\text{IDLE}_1, \text{IDLE}_2, \text{AC}, \text{HEAT}\}) \\ \text{Check}(\text{Above} \vee \text{Below}) &= (\{\text{AC}, \text{HEAT}\}, \{\text{OFF}, \text{IDLE}_1\}, \{\text{IDLE}_2\}) \\ \text{Check}(\text{AX } \neg \text{HEAT}) &= (\{\text{OFF}, \text{AC}\}, \{\}, \{\text{IDLE}_1, \text{IDLE}_2, \text{HEAT}\}) \end{aligned}$$

Function  $\text{QUntil}()$  determines the value of  $EU$  and  $AU$  using a fixpoint algorithm given in Figure 5. It starts with assigning  $QU_0$  the lowest ("most false") value it can attain, i.e., the value of  $\psi$ . At each iteration, the algorithm computes  $\text{EXTerm}_{i+1}$ , equal to  $\text{EX}QU_i$ . If the function is called with the universal quantifier, then it also computes  $\text{AXTerm}_{i+1}$ , equal to  $\text{AX}QU_i$ . Otherwise,  $\text{AXTerm}_{i+1}$  is not necessary, and thus we let  $\text{AXTerm}_{i+1}$  be  $[\varphi]$ .  $\text{AX}QU_i$  and  $\text{EX}QU_i$  are computed by invoking the function  $\text{doBigOp}()$  and passing it the result of the appropriate  $\text{pred}$  call. Then, for each state  $s$ , the algorithm determines where this state should be by computing  $\text{dest} := \langle \psi \rangle_s \sqcup (\langle \varphi \rangle_s \sqcap [\text{AXTerm}_{i+1}]_s \sqcap [\text{EXTerm}_{i+1}]_s)$ . If  $\text{dest}$  value is different from the one  $s$  had in  $QU_i$ , then it has to be moved to the appropriate place in  $QU_{i+1}$ . The algorithm proceeds until no further changes to  $QU_i$  can be made.

For example, suppose we are computing  $E[\neg \text{Below } U \text{ Heat}]$  for our model in Figure 1(c) under the ordering  $\text{T} < \text{M} < \text{F}$ .  $QU_0$  is initialized to  $(\{\text{HEAT}\}, \{\text{AC}\}, \{\text{OFF}, \text{IDLE}_1, \text{IDLE}_2\})$ .  $\text{IDLE}_2$  has  $\text{HEAT}$  among its successors, so  $[\text{EXTerm}_1]_{\text{IDLE}_2}$  is T. Thus,

$$[\text{Heat}]_{\text{IDLE}_2} \sqcup ([\neg \text{Below}]_{\text{IDLE}_2} \sqcap [\text{EXTerm}_1]_{\text{IDLE}_2}) = \text{F} \sqcup (\text{T} \sqcap \text{T}) = \text{T}$$

and so  $\text{IDLE}_2$  should move to T. Using a similar process, we decide that  $\text{dest}$  for  $\text{IDLE}_1$  in  $QU_1$  is M, and that  $\text{dest}$  for AC and OFF in  $QU_2$  are T and M, respectively. The next iteration does not change  $QU_2$ , and thus the algorithm terminates returning  $(\{\text{HEAT}, \text{AC}, \text{IDLE}_2\}, \{\text{OFF}, \text{IDLE}_1\}, \{\})$ .

Property	$\chi$ CTL formulation	Heater Model	AC Model	Combined Model
Prop. 1.	$AG \text{ EX IDLE}_1$	F	T	F
Prop. 2.	$A [\neg \text{Heat} U \text{Below}]$	T	—	T
Prop. 3.	$AG \text{ AF } \neg \text{Running}$	F	F	F
Prop. 4.	$AG (\text{Heat} \leftrightarrow \text{AC})$	—	—	F
Prop. 5.	$AG (\text{Above} \rightarrow \neg \text{Heat})$	—	—	M

**Table 1.** Results of verifying properties of the thermostat system.

The properties of the thermostat system that we identified in Section 2 can be translated into  $\chi$ CTL as described in Table 1. The table also lists the values of these properties in each of the models given in Figure 1. We use “—” to indicate that the result cannot be obtained from this model. For example, the two individual models disagree on the question of reachability of state  $\text{IDLE}_1$  from every state in the model, whereas the combined model concludes that it is F.

## 7 Correctness and Termination of $\chi$ chek

In this section, we analyze running time of  $\chi$ chek and prove its correctness and termination.

### 7.1 Complexity

**Theorem 4** *Procedure  $\text{Check}(p)$  terminates on every  $\chi$ CTL formula  $p$ .*

Computation of Until takes the longest time. Each state can change its position in  $QU_i$  at most  $h$  times, where  $h$  is the height of the lattice  $(\mathcal{L}, \sqsubseteq)$ . Thus, the maximum number of iterations of the loop in  $\text{QUntil}$  is  $|S| \times h$ . Each iteration takes the time to compute  $\text{doBigOP}$  on  $\text{pred}$ :  $O(|\mathcal{L}| \times 2^{|\mathcal{L}|} \times |S| + |\mathcal{L}|^2 \times |S|^2)$ , plus the time to compute  $\text{toMove}$  and  $\text{dest}$  sets:  $|\mathcal{L}|^4 \times O(|S|)$ . Therefore, the running time of  $\text{QUntil}$  is

$$O(2^{|\mathcal{L}|} \times |S|^2 \times |S| \times h) = O(2^{|\mathcal{L}|} \times |S|^3)$$

and the running time of the entire model-checking algorithm on a property  $p$  is

$$O(2^{|\mathcal{L}|} \times |S|^3 \times |p|)$$

Note that in reality the running time is smaller, because  $\text{BigOPTable}$  can be optimized and because set operations are BDD-based.

<b>Procedure</b> BooleanCheck( $p$ )	
<b>Case</b>	
$p \in A$	: <b>return</b> $I(p, \top)$
$p = \neg \varphi$	: <b>return</b> $(S - \varphi)$
$p = \varphi \wedge \psi$	: <b>return</b> $(\varphi \cap \psi)$
$p = \varphi \vee \psi$	: <b>return</b> $(\varphi \cup \psi)$
$p = EX \varphi$	: <b>return</b> $\text{pre}(\varphi)$
$p = AX \varphi$	: <b>return</b> $(S - \text{pre}(S - \varphi))$
$p = E[\varphi U \psi]$	: $Q_0 = \emptyset$ $Q_{i+1} = Q_i \cup (\psi \vee (\varphi \wedge EX Q_i))$ <b>return</b> $Q_n$ when $Q_n = Q_{n+1}$
$p = A[\varphi U \psi]$	: $Q_0 = \emptyset$ $Q_{i+1} = Q_i \cup (\psi \vee (\varphi \wedge EX Q_i \wedge AX Q_i))$ <b>return</b> $Q_n$ when $Q_n = Q_{n+1}$

**Fig. 6.** Boolean Model-Checking Algorithm(adapted from [6]).

## 7.2 Correctness

In this section we prove correctness of  $\chi\text{chek}$  by showing that it always returns exactly one answer (well-foundedness) and that this answer is correct, i.e., it preserves the properties of  $\chi\text{CTL}$ . We also show that multi-valued model-checking reduces to well-known boolean model-checking [22] if  $(\mathcal{L}, \sqsubseteq)$  is the two-valued lattice representing classical logic.

We start by determining that procedure **Check**() associates each state  $s$  with exactly one logical value for each  $\chi\text{CTL}$  property  $p$ .

**Theorem 5** *The answer returned by procedure **Check**() is always well-founded, i.e.*

- (a)  $\forall p \in P, \forall s \in S, \exists v_i \in \mathcal{L}, \text{s.t. } s \in [\text{Check}(p)]_{v_i}$  (Each state in one set)
- (b)  $\forall p \in P, \forall s \in S, \exists v_i, v_j \in \mathcal{L}, \text{s.t.}$   
 $(s \in [\text{Check}(p)]_{v_i} \wedge s \in [\text{Check}(p)]_{v_j}) \rightarrow v_i = v_j$  (Each state only in one set)

Now we show that our algorithm preserves the expected properties of  $\chi\text{CTL}$  formulas given in Figure 3.

**Theorem 6**  *$\chi\text{chek}$  preserves the negation of “next” property, i.e.*

$$\forall s \in S, s \in [\text{Check}(AX \varphi)]_v \Leftrightarrow s \in [\text{Check}(EX \neg \varphi)]_{\neg v}$$

**Theorem 7**  *$\chi\text{chek}$  preserves fixpoint properties of  $AU$  and  $EU$ , i.e.*

- (1)  $\forall s \in S, \langle \text{Check}(A[\varphi U \psi]) \rangle_s = \langle \text{Check}(\psi) \rangle_s \sqcup (\langle \text{Check}(\varphi) \rangle_s \cap \langle \text{Check}(AX A[\varphi U \psi]) \rangle_s)$   
 $\cap \langle \text{Check}(EX A[\varphi U \psi]) \rangle_s)$
- (2)  $\forall s \in S, \langle \text{Check}(E[\varphi U \psi]) \rangle_s = \langle \text{Check}(\psi) \rangle_s \sqcup (\langle \text{Check}(\varphi) \rangle_s \cap \langle \text{Check}(EX E[\varphi U \psi]) \rangle_s)$

$\perp$  “until” follows easily from  $AU$  and  $EU$  fixpoints.

Our last correctness criterium is that the answers given by  $\chi\text{chek}$  on  $(2\text{-Bool}, \sqsubseteq)$ , a two-valued boolean lattice representing classical logic, are the same as given by a regular symbolic model-checker. We start by defining a “boolean symbolic model-checker”

on Kripke structures, following [6] and changing some notation to make it closer to the one used in this paper. In particular, labeling functions used in boolean model-checking typically map a formula into a set of states where it is true, with the assumption that it is false in all other states. Thus,  $\varphi$  maps into  $\langle \varphi \rangle_{\top}$  in our notation. The algorithm is given in Figure 6, with  $\text{pre}$  defined as follows:

$$\text{pre}(Q) \equiv \{s \mid t \in Q \wedge (s, t) \in R\}$$

That is,  $\text{pre}(Q)$  computes all the states that can reach elements in  $Q$  in one step.

**Theorem 8**  *$\chi\text{chek}$ , called on  $(2\text{-Bool}, \sqsubseteq)$ , returns the same answers as a boolean model-checker. More precisely,  $\forall s \in S, \forall p \in P$ ,*

- (1)  $s \in [\text{Check}(p)]_{\top} \Rightarrow s \in \text{BooleanCheck}(p)$
- (2)  $s \in [\text{Check}(p)]_{\perp} \Rightarrow s \notin \text{BooleanCheck}(p)$

## 8 Conclusion and Future Work

Multi-valued logics are a useful tool for describing models that contain incomplete information or inconsistency. In this paper we presented an extension of classical CTL model-checking to reasoning about arbitrary quasi-boolean logics. We also described an implementation of a symbolic multi-valued model-checker  $\chi\text{chek}$  and proved its termination and correctness.

We plan to extend the work presented here in a number of directions to ensure that  $\chi\text{chek}$  can effectively reason about non-trivial systems. We will start by addressing some of the limitations of our MV-Kripke structures. In particular, so far we have assumed that our variables are of the same type, with elements described by values of the lattice associated with that machine. We need to generalize this approach to variables of different types.

Further, in this work we have only addressed single-processor models. We believe that synchronous systems can be easily handled by our framework, and it is essential to extend our model-checking engine to reasoning about synchronous as well as asynchronous systems.

We are also in the process of defining and studying a number of optimizations for storage and retrieval of logic tables. These optimizations and the use of the  $\chi\text{DD}$  library do not change the worst-case running-time of  $\chi\text{chek}$ , computed in Section 7. However, they significantly affect average-case running time. Once the implementation of the model-checker is complete, we intend to conduct a series of case studies to ensure that it scales up to reasoning about non-trivial systems.

Finally, we are interested in studying the properties of  $\chi\text{chek}$  in the overall framework of  $\chi\text{bel}$ . This framework involves reasoning about multiple inconsistent descriptions of a system. We are interested in characterizing the relationship between the types of merge of individual descriptions and the interpretation of answers given by  $\chi\text{chek}$  on the merged model.



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## A Appendix

In this appendix we give proofs for the theorems appearing in the main body of the paper. The proofs follow the calculational style of [11]. Section A.1 presents proofs of theorems of lattice theory; Section A.2 gives proofs of correctness of the definition of  $\chi$ CTL operators; Section A.3 lists properties of logic tables computed for  $\chi$ chek; finally, Section A.4 uses the properties given in Section A.3 to prove correctness and termination of the implementation of  $\chi$ chek.

### A.1 Lattice Theory

Lattices have a number of properties that hold for them. We list several of them here, without proof.

$$\begin{array}{ll}
 a \sqsubseteq b \Leftrightarrow \forall c, c \sqsubseteq a \Rightarrow c \sqsubseteq b & (\sqsubseteq \text{ introduction}) \\
 a \sqsubseteq b \Leftrightarrow \forall c, a \sqsubseteq c \Leftarrow b \sqsubseteq c & (\sqsubseteq \text{ introduction}) \\
 a \sqcap b \sqsubseteq b \text{ and } a \sqcap b \sqsubseteq a & (\sqcap \text{ elimination}) \\
 a \sqsubseteq b \wedge a \sqsubseteq c \Rightarrow a \sqsubseteq b \sqcap c & (\sqcap \text{ introduction}) \\
 a \sqsubseteq a \sqcup b \text{ and } b \sqsubseteq a \sqcup b & (\sqcup \text{ introduction}) \\
 a \sqsubseteq c \wedge b \sqsubseteq c \Rightarrow a \sqcup b \sqsubseteq c & (\sqcup \text{ elimination}) \\
 a \sqsubseteq b \Leftrightarrow a \sqcup b = b & (\text{correspondence}) \\
 a \sqsubseteq b \Leftrightarrow a \sqcap b = a & (\text{correspondence})
 \end{array}$$

The following are the properties of the product of two lattices  $(\mathcal{L}_1, \sqsubseteq)$  and  $(\mathcal{L}_2, \sqsubseteq)$ :

$$\begin{array}{ll}
 \perp_{\mathcal{L}_1 \times \mathcal{L}_2} = (\perp_{\mathcal{L}_1}, \perp_{\mathcal{L}_2}) & (\perp \text{ of pairs}) \\
 \top_{\mathcal{L}_1 \times \mathcal{L}_2} = (\top_{\mathcal{L}_1}, \top_{\mathcal{L}_2}) & (\top \text{ of pairs}) \\
 \neg(a, b) = (\neg a, \neg b) & (\neg \text{ of pairs}) \\
 (a, b) \sqcap (a', b') = (a \sqcap a', b \sqcap b') & (\sqcap \text{ of pairs}) \\
 (a, b) \sqcup (a', b') = (a \sqcup a', b \sqcup b') & (\sqcup \text{ of pairs})
 \end{array}$$

**Theorem 1.** *A product of two quasi-boolean lattices is quasi-boolean, that is,*

$$\begin{aligned} (1) \quad & \neg\neg(a, b) = (a, b) \\ (2) \quad & \neg((a_1, b_1) \sqcap (a_2, b_2)) = (\neg a_1, \neg b_1) \sqcup (\neg a_2, \neg b_2) \\ (3) \quad & \neg((a_1, b_1) \sqcup (a_2, b_2)) = (\neg a_1, \neg b_1) \sqcap (\neg a_2, \neg b_2) \end{aligned}$$

**Proof:**

$$\begin{aligned} (1) \quad & \neg\neg(a, b) \\ \Leftrightarrow & (\neg \text{ of pairs}) \\ & \neg(\neg a, \neg b) \\ \Leftrightarrow & (\neg \text{ of pairs}), (\neg \text{ involution}) \\ & (a, b) \end{aligned} \qquad \begin{aligned} (2) \quad & \neg((a_1, b_1) \sqcap (a_2, b_2)) \\ \Leftrightarrow & (\sqcap \text{ of pairs}) \\ & \neg((a_1 \sqcap a_2), (b_1 \sqcap b_2)) \\ \Leftrightarrow & (\neg \text{ of pairs}) \\ & (\neg(a_1 \sqcap a_2), \neg(b_1 \sqcap b_2)) \\ \Leftrightarrow & (\text{de Morgan}) \\ & (\neg a_1 \sqcup \neg a_2, \neg b_1 \sqcup \neg b_2) \\ \Leftrightarrow & (\sqcup \text{ of pairs}) \\ & (\neg a_1, \neg b_1) \sqcup (\neg a_2, \neg b_2) \end{aligned}$$

The proof for (3) is similar. □

**Theorem 2.** *Let  $(\mathcal{L}, \sqsubseteq)$  be a horizontally-symmetric lattice. Then the following hold for any two elements  $a, b \in \mathcal{L}$ :*

$$\begin{aligned} H(a \sqcap b) &= H(a) \sqcup H(b) \\ H(a \sqcup b) &= H(a) \sqcap H(b) \end{aligned}$$

**Proof:**

We will prove the first of these equations here. The second one is a dual. We show

$$\begin{aligned} (1) \quad & H(a \sqcap b) \sqsubseteq H(a) \sqcup H(b) \quad (1) \\ (2) \quad & H(a \sqcap b) \sqsupseteq H(a) \sqcup H(b) \quad (2) \end{aligned}$$

$$\begin{aligned} (1) \quad & H(a \sqcap b) \sqsubseteq H(a) \sqcup H(b) \\ \Leftarrow & (\sqsubseteq \text{ introduction}) \\ & \forall z, (H(a \sqcap b) \sqsubseteq H(z)) \Leftarrow (H(a) \sqcup H(b) \sqsubseteq H(z)) \\ \Leftarrow & (H \text{ is order-embedding}) \\ & \forall z, (z \sqsubseteq a \sqcap b) \Leftarrow (H(a) \sqcup H(b) \sqsubseteq H(z)) \\ \Leftarrow & (\sqcap \text{ elimination}) \\ & \forall z, (z \sqsubseteq a \wedge z \sqsubseteq b) \Leftarrow (H(a) \sqcup H(b) \sqsubseteq H(z)) \\ \Leftarrow & (H \text{ is order-embedding}) \\ & \forall z, (H(a) \sqsubseteq H(z) \wedge H(b) \sqsubseteq H(z)) \Leftarrow (H(a) \sqcup H(b) \sqsubseteq H(z)) \\ \Leftarrow & (\sqcup \text{ introduction, since } H(a) \sqsubseteq H(a) \sqcup H(b), \text{ (transitivity)}) \\ & \top \\ (2) \quad & H(a) \sqcup H(b) \sqsubseteq H(a \sqcap b) \\ \Leftarrow & (\sqcup \text{ elimination}) \\ & H(a) \sqsubseteq H(a \sqcap b) \wedge H(b) \sqsubseteq H(a \sqcap b) \\ \Leftarrow & (H \text{ is order-embedding}) \\ & a \sqcap b \sqsubseteq a \wedge a \sqcap b \sqsubseteq b \\ \Leftarrow & (\sqcap \text{ elimination}) \\ & \top \end{aligned}$$

□

## A.2 $\chi$ CTL

**Theorem 3.** *Definitions of  $\langle EX\varphi \rangle_s$  and  $\langle AX\varphi \rangle_s$  preserve the negation of “next” property, i.e.*

$$\forall s \in S, \neg \langle AX\varphi \rangle_s = \langle EX\neg\varphi \rangle_s$$

**Proof:**

Let  $s \in S$  be an arbitrary state. Then,

$$\begin{aligned} & \neg \langle AX\varphi \rangle_s \\ = & \text{(definition of } AX) \\ & \neg (\bigwedge_{t \in \text{succ}(s)} (\langle s, t \rangle \rightarrow \langle \varphi \rangle_t)) \\ = & \text{(de Morgan), because } \neg \text{ is a quasi-boolean operator} \\ & \bigvee_{t \in \text{succ}(s)} \neg (\langle s, t \rangle \rightarrow \langle \varphi \rangle_t) \\ = & \text{(definition of } \rightarrow), \text{ (de Morgan)} \\ & \bigvee_{t \in \text{succ}(s)} (\neg \neg \langle s, t \rangle \wedge \neg \langle \varphi \rangle_t) \\ = & \text{(} \neg \text{ involution), because } \neg \text{ is a quasi-boolean operator} \\ & \bigvee_{t \in \text{succ}(s)} (\langle s, t \rangle \wedge \neg \langle \varphi \rangle_t) \\ = & \text{(definition of } \langle \neg\varphi \rangle_s) \\ & \bigvee_{t \in \text{succ}(s)} (\langle s, t \rangle \wedge \langle \neg\varphi \rangle_t) \\ = & \text{(definition of } EX) \\ & \langle EX\neg\varphi \rangle_s \end{aligned}$$

□

## A.3 Table Library

Here we give properties of inverse and BigOP tables defined in Section 6.1.

**Lemma 1.** *The following are properties of inverse tables, with  $op \in \{\wedge, \vee, \rightarrow\}$ :*

$$\begin{aligned} \forall v \in \mathcal{L}, (a, b) \in \text{InvTable}_{\rightarrow, v} & \Leftrightarrow (a, \neg b) \in \text{InvTable}_{\wedge, \neg v} & (\rightarrow \text{ of InvTable}) \\ \forall v \in \mathcal{L}, (a, b) \in \text{InvTable}_{\wedge, v} & \Leftrightarrow (\neg a, \neg b) \in \text{InvTable}_{\vee, \neg v} & (\text{de Morgan of InvTable}) \\ \forall v \in \mathcal{L}, \text{InvTable}_{op, v} & \neq \emptyset & (\text{non - emptiness of InvTable}) \\ \forall v_1, v_2 \in \mathcal{L}, \exists v_3 \in \mathcal{L}, \text{s.t. } (v_1, v_2) & \in \text{InvTable}_{op, v_3} & (\text{completeness of InvTable}) \\ \forall v_1, v_2, v_3, v_4 \in \mathcal{L}, ((v_1, v_2) \in \text{InvTable}_{op, v_3} \wedge & \\ (v_1, v_2) \in \text{InvTable}_{op, v_4}) & \rightarrow (v_3 = v_4) & (\text{uniqueness of InvTable}) \end{aligned}$$

**Proof:**

From the definition of inverse tables, negation properties, the definition of  $\rightarrow$  and lattice properties. □

**Lemma 2.** *The following are the properties of BigOP tables, with  $op \in \{\wedge, \vee\}$ :*

$$\begin{aligned} \forall v \in \mathcal{L}, & (\emptyset \in \text{BigOPTable}_{\vee, v} \Leftrightarrow \mathcal{L} \in \text{BigOPTable}_{\wedge, \neg v}) \wedge \\ & (\forall V \in \mathcal{P}(\mathcal{L}) - \emptyset, V \in \text{BigOPTable}_{\vee, v} \Leftrightarrow \\ & \quad \{\neg v \mid v \in V\} \in \text{BigOPTable}_{\wedge, \neg v}) & (\text{negation of BigOPTable}) \\ \forall v \in \mathcal{L}, & (\emptyset \in \text{BigOPTable}_{\wedge, v} \Leftrightarrow \mathcal{L} \in \text{BigOPTable}_{\vee, \neg v}) \wedge \\ & (\forall V \in \mathcal{P}(\mathcal{L}) - \emptyset, V \in \text{BigOPTable}_{\wedge, v} \Leftrightarrow \\ & \quad \{\neg v \mid v \in V\} \in \text{BigOPTable}_{\vee, \neg v}) & (\text{negation of BigOPTable}) \end{aligned}$$

$$\begin{aligned}
\forall v \in \mathcal{L}, \quad \text{BigOPTable}_{op,v} &\neq \emptyset && \text{(non-emptiness of BigOPTable)} \\
\forall V \in \mathcal{P}(\mathcal{L}), \exists v \in \mathcal{L}, \text{ s.t. } V &\in \text{BigOPTable}_{op,v} && \text{(completeness of BigOPTable)} \\
\forall V \in \mathcal{P}(\mathcal{L}), \forall v_1, v_2 \in \mathcal{L}, (V \in \text{BigOPTable}_{op,v_1} \wedge & \\
V \in \text{BigOPTable}_{op,v_2}) &\rightarrow (v_1 = v_2) && \text{(uniqueness of BigOPTable)}
\end{aligned}$$

**Proof:**

By construction of BigOP tables and by the idempotency property of lattices.  $\square$

**Lemma 3.** *The following are the properties of predecessor relations:*

$$\begin{aligned}
\forall \varphi, \forall s \in S, \exists v \in \mathcal{L}, \text{ s.t. } s &\in [\text{pred}([\varphi], op)]_v \quad \text{(completeness of pred)} \\
[\text{pred}([\varphi], \wedge)]_v &= [\text{pred}([\neg\varphi], \rightarrow)]_{\neg v} \quad \text{(implication of pred)}
\end{aligned}$$

**Proof:**

(completeness of pred)

Pick  $\varphi$ , pick a state  $s$ , pick  $t \in \text{succ}(s)$ . Then, let  $v_1 = \langle s, t \rangle$  and  $v_2 = \langle \varphi \rangle_t$ . Further, let  $v = v_1 \text{ op } v_2$ . Then, by the completeness of InvTable property,  $s \in [\text{pred}([\varphi], op)]_v$ .

(implication of pred)

Let  $s \in S$  be an arbitrary state. Then,

$$\begin{aligned}
s &\in [\text{pred}([\varphi], \wedge)]_v \\
&\Leftrightarrow \text{(definition of pred)} \\
&\quad \exists t \in S, \exists (v_1, v_2) \in \text{InvTable}_{\wedge, v}, \text{ s.t. } (s, t) \in R(v_1) \wedge \langle \varphi \rangle_t = v_2 \\
&\Leftrightarrow \text{(\(\rightarrow\) of InvTable)} \\
&\quad \exists t \in S, \exists (v_1, \neg v_2) \in \text{InvTable}_{\rightarrow, \neg v}, \text{ s.t. } (s, t) \in R(v_1) \wedge \langle \varphi \rangle_t = v_2 \\
&\Leftrightarrow \text{(definition of pred)} \\
s &\in [\text{pred}([\neg\varphi], \rightarrow)]_{\neg v}
\end{aligned}$$

$\square$

#### A.4 Correctness and Termination

**Theorem 4.** *Procedure Check( $p$ ) terminates on every XCTL formula  $p$ .*

**Proof:**

Proof is on the structure of property  $p$ . Obviously, for all operators except “until”, Check( $p$ ) terminates. We give proof for computing  $AU$  here. To prove that the execution of QUntil terminates, it suffices to show that  $\forall s \in S, \forall i, \langle QU_i \rangle_s \sqsubseteq \langle QU_{i+1} \rangle_s$ . Then,  $QU_i$  can change value at most  $h$  times, where  $h$  is the height of lattice  $(\mathcal{L}, \sqsubseteq)$ .

The proof goes by induction on  $i$ . Pick  $s \in S$ . Then,

$$\begin{aligned}
\text{Base case: } &\langle QU_0 \rangle_s \\
&= \text{(definition of QUntil)} \\
&\quad \langle \psi \rangle_s \\
&\sqsubseteq \text{(monotonicity of } \sqcap, \sqcup) \\
&\quad \langle \psi \rangle_s \sqcup (\langle \varphi \rangle_s \sqcap \langle \text{AXTerm}_1 \rangle_s \sqcap \langle \text{EXTerm}_1 \rangle_s) \\
&= \text{(definition of QUntil)} \\
&\quad \langle QU_1 \rangle_s \\
\text{IH: } &\langle QU_i \rangle_s \sqsubseteq \langle QU_{i+1} \rangle_s
\end{aligned}$$

Prove:  $\langle QU_{i+1} \rangle_s \sqsubseteq \langle QU_{i+2} \rangle_s$   
 Proof:  $\langle QU_{i+1} \rangle_s$   
 $=$  (definition of  $Q\text{Until}$ )  
 $\langle \psi \rangle_s \sqcup ((\langle \varphi \rangle_s \sqcap \langle AX\text{Term}_{i+1} \rangle_s \sqcap \langle EX\text{Term}_{i+1} \rangle_s)$   
 $=$  (definition of  $Q\text{Until}$ )  
 $\langle \psi \rangle_s \sqcup ((\langle \varphi \rangle_s \sqcap \prod_{t \in \text{succ}(s)} (\neg \langle s, t \rangle \sqcup \langle QU_i \rangle_s) \sqcap \bigsqcup_{t \in \text{succ}(s)} (\langle s, t \rangle \sqcap \langle QU_i \rangle_s))$   
 $\sqsubseteq$  (IH), (monotonicity)  
 $\langle \psi \rangle_s \sqcup ((\langle \varphi \rangle_s \sqcap \prod_{t \in \text{succ}(s)} (\neg \langle s, t \rangle \sqcup \langle QU_{i+1} \rangle_s) \sqcap \bigsqcup_{t \in \text{succ}(s)} (\langle s, t \rangle \sqcap \langle QU_{i+1} \rangle_s))$   
 $=$  (definition of  $Q\text{Until}$ )  
 $\langle \psi \rangle_s \sqcup ((\langle \varphi \rangle_s \sqcap \langle AX\text{Term}_{i+2} \rangle_s \sqcap \langle EX\text{Term}_{i+2} \rangle_s)$   
 $=$  (definition of  $Q\text{Until}$ )  
 $\langle QU_{i+2} \rangle_s$

□

**Theorem 5.** *The answer returned by function Check is always well-founded, i.e.*

- (a)  $\forall p \in P, \forall s \in S, \exists v_i \in \mathcal{L}, \text{s.t. } s \in [\text{Check}(p)]_{v_i}$  (Each state in one set)  
 (b)  $\forall p \in P, \forall s \in S, \exists v_i, v_j \in \mathcal{L}, \text{s.t.}$   
 $(s \in [\text{Check}(p)]_{v_i} \wedge s \in [\text{Check}(p)]_{v_j}) \rightarrow v_i = v_j$  (Each state only in one set)

**Proof:**

The proof is by induction on the length of  $p$ .

Base case:  $p \in A$ .  $\text{Check}(p)$  uses  $I$  which is guaranteed to return a partition by definition.

IH: Assume  $\text{Check}(p)$  returns a partition when  $|p| \leq n$ .

Prove:  $\text{Check}(p)$  returns a partition when  $|p| = n + 1$ .

Proof:

$p = \neg \varphi$  Then,  $\varphi()$  is a partition by IH, and  $\neg v$  is onto by  $\neg$  involution.  
 $p = \varphi \wedge \psi$  Pick state  $s \in S$ . Since  $[\varphi]$  and  $[\psi]$  are partitions,  $\exists v_1, v_2 \in \mathcal{L}$  s.t.  
 $s \in [\varphi]_{v_1}$  and  $s \in [\psi]_{v_2}$ .  
 (a) By completeness of  $\text{InvTable}$ ,  $\exists v_3, \text{s.t. } s \in \text{InvTable}_{\wedge, v_3}$ ,  
 so  $s \in [p]_{v_3}$ .  
 (b) By uniqueness of  $\text{InvTable}$ .  
 $p = \varphi \vee \psi$  The proof is similar to the one above.  
 $p = EX \varphi$ . Pick a state  $s \in S$ .  
 Create a set  $V = \{v \mid s \in [\text{pred}([\varphi], \wedge)]_v\}$   
 (a) By completeness of  $\text{BigOPTable}$ ,  $\exists v_i \in \mathcal{L}, \text{s.t. } s \in [\text{doBigOP}(\vee, \text{pred}([\varphi], \wedge))]_{v_i}$ .  
 (b) By uniqueness of  $\text{BigOPTable}$ , the above-found  $v_i$  is unique.  
 $p = AX \varphi$ . The proof is similar to the one above.  
 $p = E[\varphi U \psi]$  Partitionness is maintained as an invariant of  $Q\text{Until}$ :  
 $Q\text{Until}$  starts of with a partition, and  $\text{move}$  preserves partition.  
 $p = A[\varphi U \psi]$  Same as above.

□

**Theorem 6.**  *$\chi\text{chek}$  preserves the negation of “next” property, i.e.*

$$\forall s \in S, s \in [\text{Check}(AX \varphi)]_v \Leftrightarrow s \in [\text{Check}(EX \neg \varphi)]_{\neg v}$$

**Proof:**

We prove

$$\begin{aligned} (1) \quad s \in [\text{Check}(AX\varphi)]_v &\Rightarrow s \in [\text{Check}(EX\neg\varphi)]_{\neg v} \\ (2) \quad s \in [\text{Check}(EX\neg\varphi)]_{\neg v} &\Rightarrow s \in [\text{Check}(AX\varphi)]_v \end{aligned}$$

$$\begin{aligned} &(1) \quad s \in [\text{Check}(AX\varphi)]_v \\ \Rightarrow & \text{(definition of Check)} \\ & s \in [\text{doBigOP}(\wedge, \text{pred}([\varphi], \rightarrow))]_v \\ \Rightarrow & \text{(definition of doBigOP)} \\ & \exists V \in [\text{BigOPTable}_\wedge]_v, \text{ s.t. } (s \in \bigcap_{v_i \in V} [\text{pred}([\varphi], \rightarrow)]_{v_i}) \wedge \\ & \quad (s \notin \bigcup_{v_i \in (\mathcal{L} - V)} [\text{pred}([\varphi], \rightarrow)]_{v_i}) \\ \Rightarrow & \text{(negation of BigOPTable)} \\ & \exists V_1 \in [\text{BigOPTable}_\vee]_{\neg v}, \text{ s.t. } (s \in \bigcap_{\neg v_i \in V_1} [\text{pred}([\varphi], \rightarrow)]_{v_i}) \wedge \\ & \quad (s \notin \bigcup_{\neg v_i \in (\mathcal{L} - V_1)} [\text{pred}([\varphi], \rightarrow)]_{v_i}) \\ \Rightarrow & \text{(implication of pred)} \\ & \exists V_1 \in [\text{BigOPTable}_\vee]_{\neg v}, \text{ s.t. } (s \in \bigcap_{\neg v_i \in V_1} [\text{pred}([\neg\varphi], \wedge)]_{\neg v_i}) \wedge \\ & \quad (s \notin \bigcup_{\neg v_i \in (\mathcal{L} - V_1)} [\text{pred}([\neg\varphi], \wedge)]_{\neg v_i}) \\ \Rightarrow & \text{(definition of doBigOP)} \\ & s \in [\text{doBigOP}(\vee, \text{pred}([\neg\varphi], \wedge))]_{\neg v} \\ \Rightarrow & \text{(definition of Check)} \\ & s \in [\text{Check}(EX[\neg\varphi])]_{\neg v} \end{aligned}$$

Proof of (2) is similar, and is based on implication of `pred` and negation of `BigOPTable`.  $\square$

**Theorem 7.**  *$\chi\text{chek}$  preserves fixpoint properties of  $AU$  and  $EU$ , i.e.*

$$\begin{aligned} (1) \quad \forall s \in S, \langle \text{Check}(A[\varphi U \psi]) \rangle_s &= \langle \text{Check}(\psi) \rangle_s \sqcup (\langle \text{Check}(\varphi) \rangle_s \sqcap \langle \text{Check}(AXA[\varphi U \psi]) \rangle_s \\ &\quad \sqcap \langle \text{Check}(EXA[\varphi U \psi]) \rangle_s) \\ (2) \quad \forall s \in S, \langle \text{Check}(E[\varphi U \psi]) \rangle_s &= \langle \text{Check}(\psi) \rangle_s \sqcup (\langle \text{Check}(\varphi) \rangle_s \sqcap \langle \text{Check}(EXE[\varphi U \psi]) \rangle_s) \end{aligned}$$

**Proof:**

Pick a state  $s$ .

$$\begin{aligned} &(1) \quad s \in [\text{Check}(A[\varphi U \psi])]_v \\ \Leftrightarrow & \text{(definition of Check)} \\ & s \in [\text{QUntil}(A, [\varphi], [\psi])]_v \\ \Leftrightarrow & \text{(definition of QUntil)} \\ & \exists n > 0, \text{ s.t., } QU_{n+1} = QU_n \\ & \wedge s \in [QU_{n+1}]_v \Leftrightarrow (v = \langle \text{Check}(\psi) \rangle_s \sqcup (\langle \text{Check}(\varphi) \rangle_s \sqcap \langle \text{AXTerm}_{n+1} \rangle_s \sqcap \langle \text{EXTerm}_{n+1} \rangle_s)) \\ \Leftrightarrow & \text{(definition of AXTerm), (definition of EXTerm), (definition of AX in Check)} \\ & \exists n > 0, \text{ s.t., } QU_{n+1} = QU_n \\ & \wedge s \in [QU_{n+1}]_v \Leftrightarrow (v = \langle \text{Check}(\psi) \rangle_s \sqcup (\langle \text{Check}(\varphi) \rangle_s \sqcap \langle \text{Check}(AXQU_n) \rangle_s \\ & \quad \sqcap \langle \text{Check}(EXQU_n) \rangle_s)) \\ \Leftrightarrow & \text{(combining the two conjuncts)} \\ & s \in [QU_n]_v \Leftrightarrow (v = \langle \text{Check}(\psi) \rangle_s \sqcup (\langle \text{Check}(\varphi) \rangle_s \sqcap \langle \text{Check}(AXQU_n) \rangle_s \sqcap \langle \text{Check}(EXQU_n) \rangle_s)) \\ \Leftrightarrow & (A[\varphi U \psi] = QU_n) \\ & \langle \text{Check}(A[\varphi U \psi]) \rangle_s = \langle \text{Check}(\psi) \rangle_s \sqcup \\ & \quad (\langle \text{Check}(\varphi) \rangle_s \sqcap \langle \text{Check}(AXA[\varphi U \psi]) \rangle_s \sqcap \langle \text{Check}(EXE[\varphi U \psi]) \rangle_s) \end{aligned}$$

Proof of (2) is similar.  $\square$

In our last theorem we want to prove that the result of calling  $\chi\text{chek}$  with (2-Bool,  $\sqsubseteq$ ), a lattice representing classical logic, is the same as the result of the boolean CTL model-checker.

We start by defining inverse and BigOP tables for a boolean lattice:

$$\begin{array}{ll} \text{InvTable}_{\wedge, \top} = \{(\top, \top)\} & \text{BigOPTable}_{\wedge, \top} = \{\{\top\}, \emptyset\} \\ \text{InvTable}_{\wedge, \perp} = \{(\top, \perp), (\perp, \perp), (\perp, \top)\} & \text{BigOPTable}_{\wedge, \perp} = \{\{\perp\}, \{\perp, \top\}\} \\ \text{InvTable}_{\vee, \top} = \{(\top, \perp), (\top, \top), (\perp, \top)\} & \text{BigOPTable}_{\vee, \top} = \{\{\top\}, \{\top, \perp\}\} \\ \text{InvTable}_{\vee, \perp} = \{(\perp, \perp)\} & \text{BigOPTable}_{\vee, \perp} = \{\{\perp\}, \emptyset\} \end{array}$$

**Lemma 4.** *The following relations hold for each  $s \in S$  when multi-valued model-checking is called on (2-Bool,  $\sqsubseteq$ ):*

$$\begin{array}{ll} \forall s \in S, s \in [\text{pred}([\varphi], \wedge)]_{\top} \Rightarrow s \in \text{pre}(\varphi) & (\text{property of } [\text{pred}([\varphi], \wedge)]_{\top}) \\ \forall s \in S, s \in [\text{pred}([\varphi], \rightarrow)]_{\top} \Rightarrow s \in \neg \text{pre}(\neg \varphi) & (\text{property of } [\text{pred}([\varphi], \rightarrow)]_{\top}) \\ \forall s \in S, s \in [\text{pred}([\varphi], \wedge)]_{\perp} \Rightarrow s \in \neg \text{pre}(\varphi) & (\text{property of } [\text{pred}([\varphi], \wedge)]_{\perp}) \\ \forall s \in S, s \in [\text{pred}([\varphi], \rightarrow)]_{\perp} \Rightarrow s \in \text{pre}(\neg \varphi) & (\text{property of } [\text{pred}([\varphi], \rightarrow)]_{\perp}) \end{array}$$

**Proof:**

We prove properties of  $[\text{pred}([\varphi], \wedge)]_{\top}$  and  $[\text{pred}([\varphi], \rightarrow)]_{\top}$ . The others follow from (implication of  $\text{pred}$ ). For an arbitrary state  $s \in S$ ,

$$\begin{array}{l} \text{property of } [\text{pred}([\varphi], \wedge)]_{\top}: \\ \quad s \in [\text{pred}([\varphi], \wedge)]_{\top} \\ \Rightarrow (\text{definition of pred}) \\ \quad \exists t \in S, \exists (v_1, v_2) \in \text{InvTable}_{\wedge, \top}, \text{ s.t. } (s, t) \in R(v_1) \wedge \langle \varphi \rangle_t = v_2 \\ \Rightarrow (\text{value of InvTable}_{\wedge, \top}) \\ \quad \exists t \in S, \text{ s.t. } (s, t) \in R \wedge \langle \varphi \rangle_t = \top \\ \Rightarrow (\text{definition of pre}) \\ \quad s \in \text{pre}(\varphi) \\ \text{property of } [\text{pred}([\varphi], \rightarrow)]_{\top}: \\ \quad s \in [\text{pred}([\varphi], \rightarrow)]_{\top} \\ \Rightarrow (\text{definition of pred}) \\ \quad \exists t \in S, \exists (v_1, v_2) \in \text{InvTable}_{\rightarrow, \top}, \text{ s.t. } (s, t) \in R(v_1) \wedge \langle \varphi \rangle_t = v_2 \\ \Rightarrow (\text{value of InvTable}_{\rightarrow, \top}) \\ \quad \exists t \in S, \neg((s, t) \in R(\top)) \wedge (\langle \neg \varphi \rangle_t = \top) \\ \Rightarrow (\text{definition of pre}) \\ \quad s \notin \text{pre}(\neg \varphi) \end{array}$$

□

**Theorem 8.**  *$\chi\text{chek}$ , called on (2-Bool,  $\sqsubseteq$ ), returns the same answers as a boolean model-checker. More precisely,  $\forall s \in S, \forall p \in P$ ,*

$$\begin{array}{l} (1) \ s \in [\text{Check}(p)]_{\top} \Rightarrow s \in \text{BooleanCheck}(p) \\ (2) \ s \in [\text{Check}(p)]_{\perp} \Rightarrow s \notin \text{BooleanCheck}(p) \end{array}$$

**Proof:**

The proof is by induction on the structure of property  $p$ .



Base Case:

$p \in A : \text{Check}(p)$  and  $\text{BooleanCheck}(p)$  give the same answers by definition.

IH: Assume (1) and (2) hold for properties of length  $\leq n$ .

Prove: (1) and (2) hold for properties of length  $n + 1$ .

Proof: For brevity, we omit most of the proof, showing only the proofs for  $\varphi \wedge \psi$ ,  $EX$  and  $AU$ .

$p = \neg\varphi$  :      (1):  $s \in [\text{Check}(p)]_{\top}$   
 $\Rightarrow$  (definition of  $\text{Check}$ )  
 $s \in [\varphi]_{\perp}$   
 $\Rightarrow$  (definition of  $\text{Check}$ )  
 $s \in [\text{Check}(\varphi)]_{\perp}$   
 $\Rightarrow$  (IH)  
 $s \notin \text{BooleanCheck}(\varphi)$   
 $\Rightarrow$  (definition of  $\text{BooleanCheck}$ )  
 $s \in \text{BooleanCheck}(p)$   
 Proof for (2) is similar

$p = \varphi \wedge \psi$  :      (1):  $s \in [\text{Check}(p)]_{\top}$   
 $\Rightarrow$  (definition of  $\text{doOP}$ )  
 $s \in \langle \varphi \rangle_a \wedge s \in \langle \psi \rangle_a \wedge (a, b) \in \text{InvTable}_{\wedge, \top}$   
 $\Rightarrow$  (value of  $\text{InvTable}_{\wedge, \top}$ )  
 $s \in \langle \varphi \rangle_{\top} \wedge s \in \langle \psi \rangle_{\top}$   
 $\Rightarrow$  (changing notation)  
 $s \in (\varphi \cap \psi)$   
 $\Rightarrow$  (definition of  $\text{BooleanCheck}$ )  
 $s \in \text{BooleanCheck}(p)$   
 Proof for (2) is similar. Because of the value of  $\text{InvTable}_{\wedge, \perp}$ ,  
 $s \in \langle \varphi \rangle_a \wedge s \in \langle \psi \rangle_a \wedge (a, b) \in \text{InvTable}_{\wedge, \perp}$  implies  
 that  $s \notin \langle \varphi \rangle_{\top} \vee s \notin \langle \psi \rangle_{\top}$ .

$p = \varphi \vee \psi$  :      Proofs are similar to the ones above and are based on values of  
 $\text{InvTable}_{\vee, \top}$  and  $\text{InvTable}_{\vee, \perp}$ .

$p = EX\varphi$  :      (1):  $s \in [\text{Check}(p)]_{\top}$   
 $\Rightarrow$  (definition of  $\text{Check}$ )  
 $s \in [\text{doBigOP}(\vee, \text{pred}([\varphi], \wedge))]_{\top}$   
 $\Rightarrow$  (definition of  $\text{doBigOP}$ ), (value of  $\text{BigOPTable}_{\vee, \top}$ )  
 $[\text{result}]_{\top} = \emptyset \cup ([\text{pred}(\varphi, \wedge)]_{\top} - [\text{pred}(\varphi, \wedge)]_{\perp})$   
 $\cup ([\text{pred}(\varphi, \wedge)]_{\top} \cap [\text{pred}(\varphi, \wedge)]_{\perp})$   
 $\Rightarrow$  (properties of  $[\text{pred}([\varphi], \rightarrow)]_{\top}$  and  $[\text{pred}([\varphi], \rightarrow)]_{\perp}$ )  
 $[\text{result}]_{\top} = (\text{pre}(\varphi) - (S - \text{pre}(\varphi))) \cup (\text{pre}(\varphi) \cap (S - \text{pre}(\varphi)))$   
 $=$  (set theory)  
 $s \in \text{pre}(\varphi)$   
 $\Rightarrow$  (definition of  $\text{BooleanCheck}$ )  
 $s \in \text{BooleanCheck}(p)$   
 (2):  $s \in [\text{Check}(p)]_{\perp}$   
 $\Rightarrow$  (definition of  $\text{Check}$ ), (definition of  $\text{doBigOP}$ ), (value of  $\text{BigOPTable}_{\vee, \perp}$ )  
 $[\text{result}]_{\perp} = \emptyset \cup ([\text{pred}(\varphi, \wedge)]_{\perp} - [\text{pred}(\varphi, \wedge)]_{\top})$   
 $\Rightarrow$  (properties of  $[\text{pred}([\varphi], \rightarrow)]_{\top}$  and  $[\text{pred}([\varphi], \rightarrow)]_{\perp}$ ), (logic)  
 $s \notin \text{pre}(\varphi)$   
 $\Rightarrow$  (definition of  $\text{BooleanCheck}$ )  
 $s \in \text{BooleanCheck}(p)$

$p = AX\varphi$  :      The proof of (1) and (2) is similar to the one above and is based

on properties of  $[\text{pred}([\varphi], \wedge)]_{\top}$  and  $[\text{pred}([\varphi], \wedge)]_{\perp}$ , values of  $\text{Big0PTable}_{\wedge, \top}$  and  $\text{Big0PTable}_{\wedge, \perp}$ .  
 $p = A[\varphi U \psi]$  Since  $\text{Check}(p)$  expands into computing  $QU_n$  in  $\text{QUntil}(A, [\varphi], [\psi])$ , the proof for (1) goes by induction on  $n$  — the length of the path from  $s$  to a state where  $\psi$  holds.

Base case:  $n = 0$ .

$QU_1 = QU_0 \wedge s \in [Q_0]_{\top}$   
 $\Rightarrow s \in [\psi]_{\top}$   
 (definition of  $\text{BooleanCheck}$ )  
 $\Rightarrow s \in Q_1$   
 (definition of  $\text{BooleanCheck}$ )  
 $s \in \text{BooleanCheck}(p)$   
 IH: Assume (1) holds for all  $n \leq k$ .  
 Prove: (1) holds for  $n = k + 1$ .  
 Proof:  $s \in [\text{Check}(p)]_{\perp}$   
 $\Rightarrow$  (definition of  $\text{QUntil}$ ), (definition of  $\text{EXTerm}$ ), (definition of  $\text{AXTerm}$ )  
 $(QU_{k+2} = QU_{k+1}) \wedge (\langle \psi \rangle_s \sqcup (\langle \varphi \rangle_s \sqcap \langle AXQU_{k+1} \rangle_s \sqcap \langle EXQU_{k+1} \rangle_s) = \top)$   
 $\Rightarrow$  (boolean lattice rules)  
 $(QU_{k+2} = QU_{k+1}) \wedge$   
 $(\langle \psi \rangle_s = \top \vee (\langle \varphi \rangle_s = \top \wedge \langle AXQU_{k+1} \rangle_s = \top \wedge \langle EXQU_{k+1} \rangle_s = \top))$   
 $\Rightarrow$  (Theorem 8 for  $p = AX\varphi$ ), (Theorem 8 for  $p = EX\varphi$ ), (IH)  
 $s \in \text{BooleanCheck}(\psi) \vee (s \in \text{BooleanCheck}(\varphi) \wedge$   
 $s \in \text{BooleanCheck}(AXQ_k) \wedge s \in \text{BooleanCheck}(EXQ_k))$   
 $\Rightarrow$  (definition of  $\text{BooleanCheck}$ )  
 $s \in \text{BooleanCheck}(p)$   
  
 $(2) s \in [\text{Check}(p)]_{\perp}$   
 $\Rightarrow$  (definition of  $\text{Check}$ ), (definition of  $\text{AXTerm}$ ), (definition of  $\text{EXTerm}$ )  
 $\forall i, \langle \psi \rangle_s \sqcup (\langle \varphi \rangle_s \sqcap \langle AXQU_i \rangle_s \sqcap \langle EXQU_i \rangle_s) = \perp$   
 $\Rightarrow$  (boolean lattice laws)  
 $\forall i, \langle \psi \rangle_s = \perp \wedge (\langle \varphi \rangle_s = \perp \vee \langle AXQU_i \rangle_s = \perp \vee \langle EXQU_i \rangle_s = \perp)$   
 $\Rightarrow$  (Theorem 8 for  $p = AX\varphi$ ), (Theorem 8 for  $p = EX\varphi$ ), (Base Case)  
 $\forall i, s \notin \text{BooleanCheck}(\psi) \wedge (s \notin \text{BooleanCheck}(\varphi) \vee$   
 $s \notin \text{BooleanCheck}(AXQ_i) \vee s \notin \text{BooleanCheck}(EXQ_i))$   
 $\Rightarrow$  (definition of  $\text{BooleanCheck}$ )  
 $\forall i, s \notin Q_i \Leftrightarrow s \notin \text{BooleanCheck}$   
 $p = E[\varphi U \psi]$ : The proof of (1) and (2) is similar to the one above.

□