Deductive Verification of Cryptographic Software

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Abstract

We report on the application of an off-the-shelf verification platform to the RC4 stream cipher cryptographic software implementation (as available in the openSSL library), and introduce a deductive verification technique based on self-composition for proving the absence of error propagation.

1 Introduction

Software implementations of cryptographic algorithms and protocols are at the core of security functionality in many IT products. They are also vital components in safety-critical systems, namely in the military area. However, the development of this class of software products is understudied as a domain-specific niche in software engineering. This development is clearly distinct from other areas of software engineering due to a combination of factors. Firstly, cryptography is an inherently inter-disciplinary subject. The design and implementation of cryptographic software draws on skills from mathematics, computer science and electrical engineering. Such a rich body of research is difficult to absorb and apply without error, even for the most expert software engineer. Secondly, security is notoriously difficult to sell as a feature in software products, even when clear risks such as identity theft and fraud are evident. An important implication of this fact is that security needs to be as close to invisible as possible in terms of computational and communication load. As a result, it is critical that cryptographic software is optimised aggressively, without altering the security semantics. Finally, typical software engineers develop systems focused on desktop class processors within computers in our offices and homes. Cryptographic software is implemented on a much wider range of devices, from embedded processors with very limited computational power, memory and autonomy, to high-end servers, which demand high-performance and low-latency. Not only must cryptographic software engineers understand each platform and the related security requirements, they must optimise each algorithm with respect to each platform.

CACE[1] (Computer Aided Cryptography Engineering) is an European Project that targets the lack of support currently offered to cryptographic software engineers. The central objective is the development of a tool-box of domain-specific languages, compilers and libraries, that supports the production of high quality cryptographic software. Specific components within the tool-box will address particular software development problems and processes; and combined use of the constituent tools is enabled by designed integration between their interfaces. The project started in 2008 and will run for three years.

This paper stems from CACE - Work Package 5, which aims to add formal methods technology to the tool-box, as a means to increase the degree of assurance than can be provided by the development process. We describe promising early results obtained during our exploration of existing verification techniques and tools used to construct high-assurance software implementations for other domains. Specifically, we present our achievements in using an off-the-shelf verification tool to validate security-relevant properties of a `C` implementation of the `RC4` encryption scheme that is included in the well-known open-source library `openSSL` (`http://www.openssl.org`). Additionally, we have developed a framework that permits automating most of the steps required to verify non-interference using the self-composition approach introduced in [2]. We believe this verification technique may be of independent interest.

[1] `http://www.cace-project.eu`

2 Background

Deduction-based Program Verification. The techniques employed in this paper are based on Hoare Logic [8], brought to practice through the use of *contracts* – specifications consisting of preconditions and post-conditions, annotated into the programs. In recent years verification tools based on contracts have become more and more popular, as their scope evolved from toy languages to very realistic fragments of languages like C, C#, or Java. In a nutshell, a verification infra-structure consists of a verification conditions generator (VCGen for short) and a proof tool, which may be either an automatic theorem prover or an interactive proof assistant. The VCGen reads in the annotated code (which contains contracts and other annotations meant to facilitate the verification, such as loop invariants and variants) and produces a set of proof obligations known as *verification conditions*, that are sent to the proof tool. The correctness of the VCGen guarantees that if all the proof obligations are valid then the program is correct with respect to its specification.

The concrete tools we have used in this work were `Frama-c` [3], a tool for the static analysis of `C` programs annotated using the ANSI-C Specification Language (ACSL [3]). `Frama-c` contains a multi-prover VCGen [7], that we used together with a set of proof tools that included the Coq proof assistant [17], and the Simplify [5] and Ergo [4] automatic theorem provers. Both `Frama-c` and ACSL are very much work in progress; we have used the latest release of `Frama-c` (known as Lithium).

A few features of the `Frama-c` VCGen should be emphasized in the context of our work. The first is the possibility to export individual proof obligations to different proof tools, which allows users to first try discharging them with one or more automatic provers, leaving the harder conditions to be exported to an interactive proof assistant. A second feature is the declaration of lemmata: results that can be used to prove goals, but give themselves origin to new goals. We have used this possibility to clearly isolate the parts of proofs that cannot be handled automatically. It is often the case that the difficulty resides in a single result that has to be proved interactively, but whose existence as a lemma allows for all the remaining conditions to be proved automatically. The possibility to define inductive predicates (passed on to the proof tool as axiomatized predicates, and kept as inductive definitions if the tool supports them, as is the case of Coq), and the *labels* mechanism that allows the value of an expression in a program state to be used explicitly in logical expressions, are two additional features that we have made use of.

Finally, `Frama-c` generates *safety conditions* – special verification conditions (not generated from contracts), whose validity implies that the program will execute safely with respect to, say, memory accesses or integer overflow. Safety verification may be run independently of functional verification.

Information Flow Security. Information flow security refers to a class of security policies that constrain the ways in which information can be manipulated during program execution. These properties can be formulated in terms of non-interference between high-confidentiality input variables and low-confidentiality output variables, and are usually verified using a special extended type system. A dual formulation permits capturing security policies which constrain information flow from non-trustworthy (or low-integrity) inputs, to trusted (or high-integrity) outputs. We provide a short overview of developments in this area related to the work in this paper in Section 6.

Consider the more common case of secure information-flow that aims to preserve data confidentiality. Information may flow from high-security to low-security either directly via assignment instructions, or indirectly. The following code from Terauchi and Aiken [16] computes in f_1 the n^{th} Fibonacci number and then assigns a value to l that depends on the value of f_1.

```
while (n > 0) { f1 = f1 + f2; f2 = f1 - f2; n--; }
if (f1 > k) l = 1; else l = 0;
```

Let l be low security and n high security; then clearly there is an indirect information leakage from n to l, since the assignment `l = 1` is guarded by a condition that depends on the value of f_1, and assignments to the latter variable are performed inside a loop that is controlled by the high security condition $n > 0$. The program is thus insecure. If n were not high security, the program would of course be secure.

Type-based analyses would address the problem by tracking assignments to low security variables. Observe, however, that this fails to capture subtle situations where an apparently insecure program is in fact secure. If the last line of the program were changed to `if (f1 > k) l = E1; else l = E2;` where `E1`, `E2` are two expressions that evaluate to the same value, then the program should be classified as high security, since there is no way to tell from the final value of l anything about f_1. Type-based analyses would typically fail to distinguish this from the previous program: both would be conservatively classified as insecure. An alternative approach is to define a program as secure if different terminating executions, starting from states that differ only in the values of high-security variables, result in final states that are equivalent with respect to the values of low-security variables. This approach, based on the language semantics, avoids the excessively conservative behaviour of the previous method.

More formally, let V_H and V_L denote respectively the sets of high-security and low-security variables of C, and $V'_L = \mathit{Vars}(C) \setminus V_H$. We write $(C,\sigma) \Downarrow \tau$ to denote the fact that when executed in state σ, C stops in state τ (i.e. $\Downarrow$ is the evaluation relation in a big-step semantics of the underlying language). Then the program C is secure if $\sigma \overset{V'_L}{=} \tau \ \wedge\ (C,\sigma) \Downarrow \sigma' \ \wedge\ (C,\tau) \Downarrow \tau' \implies \sigma' \overset{V_L}{=} \tau'$ for arbitrary states σ, τ, where $\sigma \overset{X}{=} \tau$ denotes the fact that $\sigma(x) = \tau(x)$ for all $x \in X$, i.e. σ and τ are V-indistinguishable.

Self-composition. The operational definition of non-interference involves two executions of the program, but it can be reformulated so that a single execution (of a transformed program) is considered, using the *self-composition* technique [2]. Given some program C, let C^s be the program that is equal to C except that every variable x is renamed to a fresh variable x^s. Non-interference can be formulated considering a single execution of the self-composed program $C;C^s$. Note that any state σ of $C;C^s$ can be partitioned into two sates with disjoint domains $\sigma = \sigma^o \cup \sigma^s$ where $\mathit{dom}(\sigma^o) = \mathit{Vars}(C)$ and $\mathit{dom}(\sigma^s) = \{x^s \mid x \in \mathit{Vars}(C)\}$. C is information-flow secure if any terminating execution of the self-composed program $C;C^s$, starting from a state σ such that σ^o and σ^s differ only in the values of high-security variables, results in a final state σ' such that σ'^o and σ'^s are equivalent with respect to the values of low-security variables. This can be formulated without referring explicitly to the state partition: if $\sigma(x) = \sigma(x^s)$ for all $x \in V'_L$ and $(C;C^s,\sigma) \Downarrow \sigma'$, then $\sigma(x) = \sigma(x^s)$ for all $x \in V_L$.

Self-composition allows for a shift from an operational semantics-based to an axiomatic semantics-based definition, since the former can be written as the Hoare logic partial correctness specification $\{\bigwedge_{x \in V'_L} x = x^s\}\, C;C^s\, \{\bigwedge_{x \in V_L} x = x^s\}$ which can be verified, for instance, as an ACSL contract.

An ACSL program can easily be written for this example. Note that ACSL contracts pertain to functions, and as such a C function must be created whose body is the self-composed program. The contract contains a precondition stating that initial values of all non-high security variables are pairwise equal (for both composed copies of the program), and a post-condition stating that the values of low security variables are pairwise equal. Running `Frama-c` on this code, with the obvious 'control' invariants for each loop (regarding the minimum value of the variables n and n^s) also annotated, produces (with safety verification turned off) a total of 10 proof obligations, 2 of which cannot be automatically discharged. Admittedly, the control invariants do not sufficiently describe what the loops do (in particular, the fact that they are calculating Fibonacci numbers), and for this reason the post-condition cannot be proved, whether `n==ns` is included in the precondition (meaning that n is not considered high-security) or not. The verification thus fails to recognize a secure program. The difficulties of applying self-composition in practice have been previously identified (see Section 6); in this paper we introduce a technique that can help in successfully applying it with the help of a deductive verification system.

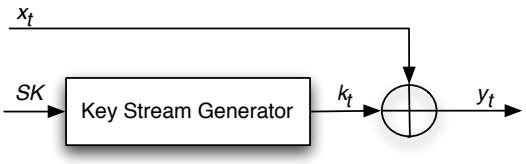

Figure 1: Block diagram of the RC4 cipher

3 RC4 and Verification of its Safety Properties in `openSSL`

`RC4` is a symmetric cipher designed by Ron Rivest at RSA labs in 1987. It is a proprietary algorithm, and its definition was never officially released. Source code that allegedly implements the `RC4` cipher was leaked on the Internet in 1994, and this is commonly known as `ARC4` due to trademark restrictions. In this work we will use the `RC4` denomination to denote the definition adopted in literature [15]. `RC4` is widely used in commercial products, as it is included as one of the recommended encryption schemes in standards such as TLS, WEP and WPA. In particular, an implementation of `RC4` is provided in the pervasively used open-source library `openSSL`, which we selected as the case study for this paper.

In cryptographic terms, `RC4` is a synchronous stream cipher, which means that it is structured as two independent blocks, as shown in Figure 1. The security of the `RC4` cipher resides in the strength of the key stream generator, which is initialized with a secret key SK. The key stream output is a byte sequence k_t that approximates a perfectly random bit string, and is independent of plaintext and ciphertext (we adopt the most widely used version of RC4, implemented in `openSSL`, which operates over byte-sized words). The encryption operation consists simply of XOR-ing each plaintext byte x_t with a fresh key stream byte k_t. Decryption operates in an identical way. The key stream generator operates over a state which includes a permutation table $S = (S[l])_{l=0}^{l=255}$ of (unsigned) byte-sized values, and two (unsigned) byte-sized indices i and j. We denote the values of these variables at time t by S_t, i_t and j_t. The state and output of the key stream generator at time t (for $t \geq 1$) are calculated according to the following recurrence, in which all additions are carried out modulo 256.

$$
\begin{aligned}
i_t &= i_{t-1} + 1 && (1)\\
j_t &= j_{t-1} + S_{t-1}[i_t] && (2)\\
S_t[i_t] &= S_{t-1}[j_t] && (3)\\
S_t[j_t] &= S_{t-1}[i_t] && (4)\\
k_t &= S_t[S_t[i_t] + S_t[j_t]] && (5)
\end{aligned}
$$

The initial values of the indices i_0 and j_0 are set to 0, and the initial value of the permutation table S_0 is derived from the secret key SK. The details of this initialisation are immaterial for the purpose of this paper, as they are excluded from the analysis.

We present in Appendix A the `C` implementation of RC4 included in the `openSSL` open-source. The function receives the current state of the RC4 key stream generator (`key`), and two arrays whose length is provided in parameter `len`. The first array contains the plaintext (`indata`), and the second array will be used to return the ciphertext (`outdata`). The same function can be used for decryption by providing the ciphertext in the `indata` buffer. We note that this implementation is much less readable than the concise description provided above, as it has been optimised for speed using various tricks, including macro inlining and loop unrolling.

Also included in the listing in Appendix A are the annotations added to this RC4 implementation in order to facilitate the verification of a set of safety properties. These comprise memory safety, includ-

ing the absence of buffer overflows, and also the absence of numeric errors due to overflows in integer calculations. This annotated version of RC4 was processed using `Frama-c`, which generated 869 verification conditions. All of these verification conditions could be automatically proved using tools such as `Alt-Ergo` and `Z3`. We highlight the following important points in this verification work.

`Frama-C` interprets C primitive types (e.g. `char`, `int`, etc.) as integers with different precisions, based on the number of bits of each type. This means that a number of proof obligations must be automatically generated to ensure the validity of each arithmetic operation, by imposing range limits on the corresponding results. Proof obligations that ensure that every memory access is safe are also automatically generated. Note that, even though these proof obligations do not result from explicit assertions made by the programmer, it is usually necessary to annotate the code with preconditions. These limit the analysis to function executions for which the caller has provided valid inputs. For example, in RC4 one must assume that the `indata` and `outdata` arrays have a valid addressable range between `0` and `len-1` for the proof obligations to be valid. In Appendix A it is visible that the required preconditions also include the validity of the memory region in which the RC4 key stream generator state (`key`) is passed to the function, and bounding the length of the input and output buffers (`len`).

The verification of the proof obligations generated by `Frama-C` also required the annotation of the RC4 code with loop invariants. These invariants are critical to enable the deductive verification process to reason about the program state before, during and after each loop execution. For example, in the first loop, the invariant permits establishing that index `i` lies between `0` and `len>>3L`, and also keeping track of how the `indata` and `outdata` pointer values change during the loop execution.

Given the high number of proof obligations to be proved, and to guide the automatic provers in the process of establishing the validity of some of these conditions, additional assertions were introduced in the code. For example, at the end of the first loop, one assertion is introduced to *force* the provers to pinpoint the condition that must be valid at the end of the loop execution.

Finally, and given that cryptographic code tends to make use of some arithmetic operators that are not commonly used in other application domains, we noted that the proof tools lacked appropriate support in some cases, namely for bit-wise operators. To overcome this difficulty we added some very simple axioms to the annotated RC4 code which express bounds on the outputs of these operators.

4 Towards Automating Proofs by Self-Composition.

Let us consider again the Fibonacci example of Section 2. The success of being able to prove automatically the security of programs using a Hoare logic-based tool such as `Frama-c` depends totally on adequately annotating the program. The ACSL program could be annotated with loop invariants describing the functional behaviour of each loop, which would require a formalisation of Fibonacci numbers. This kind of functional property would allow for the post-condition to be proved (both loops would be annotated as calculating Fibonacci numbers); however, such properties are hard to produce and not adequate for an automatic approach that should be applicable independently of what the code does.

A more feasible alternative is to use an abstract invariant that captures in logical form the state transformation associated with the loop, written as a formula that uses an inductively defined predicate (as supported by `Frama-c`). We will refer to these as the loop's *natural invariant* and *natural predicate*, respectively. Let $\vec{v}$ denote the vector of variables used inside a given loop. The predicate $natinv(\vec{v}_i, \vec{v})$ will be defined with the meaning that execution of the loop started with initial values of the variables given by $\vec{v}_i$; and the current values of the variables are given by $\vec{v}$. The inductive definition of this predicate has in general a base case of the form $natinv(\vec{v}_i, \vec{v}_i)$ (corresponding to the loop initialization) and an inductive case of the form $natinv(\vec{v}_i, \vec{v}_t) \rightarrow B \rightarrow R(\vec{v}_t, \vec{v}) \rightarrow natinv(\vec{v}_i, \vec{v})$, where B is the boolean condition of the loop, and the formula $R(\vec{v}_t, \vec{v})$ relates the values of the variables in two successive iterations.

Turning back to our example, the natural predicate of the loop, written $natinv(f_{1i}, f_{2i}, n_i, f_1, f_2, n)$, is defined inductively by the following two cases:

$$\begin{array}{rl} \text{Base case:} & \forall f_{1i}, f_{2i}, n_i.\ natinv(f_1, f_2, n, f_1, f_2, n) \\ \text{Inductive case:} & \forall f_{1i}, f_{2i}, n_i, f_{1t}, f_{2t}, n_t, f_1, f_2, n.\ natinv(f_{1i}, f_{2i}, n_i, f_{1t}, f_{2t}, n_t) \rightarrow \\ & \qquad n > 0 \rightarrow (f_1 = f_{1t} + f_{2t} \wedge f_2 = f_{1t} \wedge n = n_t - 1) \rightarrow natinv(f_{1i}, f_{2i}, n_i, f_1, f_2, n) \end{array}$$

The natural invariant of the loop is then written simply as $natinv(f_1@\mathbf{Init}, f_2@\mathbf{Init}, n@\mathbf{Init}, f_1, f_2, n)$, where $x@\mathbf{Init}$ denotes the value of x in the initial state of the loop execution. We remark that this is quite an atypical use of loop invariants, since it relates the values of variables in different states. For such a small example the definition of the natural predicate can be written by hand, but the point here is that for more realistic programs a symbolic evaluation algorithm can be used to synthesize the natural invariants.

The ACSL self-composed program annotated with the natural invariant and the inductive predicate definition is shown in Appendix B. Of the 16 VCs generated by `Frama-c`, 2 cannot be discharged automatically, and require interactive proof.

A General Framework. The hard part in the proof above is the fact that the inductive definition is deterministic in the sense that in $natinv(f_{1i}, f_{2i}, n_i, f_1, f_2, n)$ the values of f_1 and f_2 are uniquely determined by the values of f_{1i}, f_{2i}, n_i, and n (i.e. the current state of the loop is uniquely determined by the initial state and the current iteration). We isolate this as the following lemma:

$$\forall f_{1i}, f_{2i}, ni, f_{1a}, f_{2a}, n_a, f_{1b}, f_{2b}, n_b.\ natinv(f_{1i}, f_{2i}, n_i, f_{1a}, f_{2a}, n_a) \rightarrow natinv(f_{1i}, f_{2i}, n_i, f_{1b}, f_{2b}, n_b) \rightarrow n_a = n_b \rightarrow (f_{1a} = f_{1b} \wedge f_{2a} = f_{2b})$$

The lemma clearly implies that two executions of the loop starting from the same initial conditions are synchronized (in fact a weaker lemma would be sufficient for our purposes, since only the final state of the loop is relevant). Once this lemma is added to the ACSL specification, all the VCs are automatically discharged (the program is proved secure). Hoping for an automatic proof of the lemma would be too ambitious. However, we found that an interactive proof in Coq is straightforward and can be conveniently decomposed. Furthermore, such lemmata are fairly easy to write and can also be generated automatically. Based on this observation, we have developed a method to construct self-composition proofs that aims to maximise the degree of automation and essentially eliminate the need to interactively use the prover. For this, we isolate the small parts of the process that clearly require user intervention, allowing the user to just *fill-in the blanks*. In this direction, rather than stand-alone proofs for particular applications of self-composition, we have produced a Coq development that formalises natural invariants in general, and can be used to generate proofs of desired lemmata. This development will be fully explained in a long version of this paper, and can be consulted in the URL `http://crypto.di.uminho.pt/CACE/equiv/`. In a nutshell, a relational specification is first extracted from the annotated program, which is then completed with a small set of facts provided by the user. The catch is that, since these facts are kept very simple (non-inductive), they can be checked automatically. This specification is then used to instantiate Coq modules containing definitions and basic facts, from which Coq can then generate and automatically prove the desired lemma.

Reasoning with Arrays. In Section 5 we will employ the self-composition technique to the RC4 algorithm. In that context, some of the variables that must be checked to be equal at the end of the self-composed program execution are array variables (e.g. the high-integrity outputs). The appropriate notion of array equality that must be used in preconditions and post-conditions when applying the self-composition technique is of course *extensional*. Equality of array variables corresponds to equality of memory addresses, which is unsuitable to capture equality of inputs and outputs as required. Moreover,

the renaming operation C^s used by the self-composition technique must now allocate a new array for every array variable in the program C. Our Coq formalisation contemplates these aspects. An aspect of `Frama-c` that is required for natural predicates involving arrays is the possibility to have *state parameters* in predicates. The natural predicate $natinv(\vec{v_i}, \vec{v})$ was described as having as parameters the values of variables in two different states; for array variables a single array parameter is used, together with two state parameters. It is then possible to refer to the contents of array positions in those states. Having two array parameters would be wrong, since both would be passed the same memory address. Determinism lemmata as described above must also parametrised by states, and basically express properties like "if an execution of a loop starts in state S_1 and ends in S_2; a second execution of the same loop starts in state S_3 and ends in S_4; and moreover the contents of a given array is the same in states S_1 and S_3, then those contents will also be the same in states S_2 and S_4".

5 Using Information Flow to Capture Error Propagation

An important property of stream ciphers such as RC4 is how they behave when used to transfer data over channels which may introduce transmission errors. In particular, it is relevant how the decryption process reflects a wrong ciphertext symbol in the resulting plaintext: depending on the cipher construction, a ciphertext error may simply lead to a corresponding flip in a plaintext symbol, or it may affect a significant number of subsequent symbols. This property, sometimes called error propagation, is usually taken as a criterion to select ciphers for noisy communication media, where the absence of error propagation can greatly increase throughput. Note that error propagation can sometimes be seen as a desirable feature, as it amplifies errors that may be introduced maliciously, and which are therefore more easily detected.

In this section, we show how a generic technique for verifying secure information flow in software implementations can be used to check that the RC4 implementation in `openSSL` does not introduce error propagation. Given that this property is a known feature of synchronous ciphers such as RC4, it is clear that a complete proof of functional correctness with respect to a reference specification (such as the one introduced in the next section) would imply this result. However, the idea here is to demonstrate that general purpose verification tools and techniques developed for other software domains can be usefully adapted to verify relevant (and to the best of our knowledge never before addressed in literature) security properties in cryptographic software implementations.

Formalising Error Propagation as Non-Interference. The goal is to capture the error propagation property using a well studied formalism, so that we can take advantage of existing verification techniques to check this property in stream cipher implementations. To do this, we recall the notion of non-interference that was introduced in Section 2 as a possible formalisation of secure information flow. The intuition underlying this formalisation is that secure information flow can be guaranteed by checking that arbitrary changes in high-security (or low-integrity) input variables cannot be detected by observing the values of low-security (or high-integrity) output variables. Observe that the notion of a low-integrity input variable can be naturally associated with that of a transmission error over a communications channel. Hence, we map the $i^{\mathtt{th}}$ possibly erroneous ciphertext symbol to a non-trusted low-integrity input (we are looking at the decryption algorithm that, in the case of RC4, is identical to the one used for encryption). The non-interference definition can then conveniently be used to naturally capture the absence of error propagation. For this, we associate the output plaintext symbols starting at position $i+1$ to trusted high-integrity outputs. More precisely, our formulation captures the following idea: if an arbitrary change in the $i^{\mathtt{th}}$ input ciphertext symbol cannot be observed in the output plaintext symbols following position i, this implies that the stream cipher does not introduce error propagation. In other words, we want to verify that an erroneous (possibly tampered) input symbol, which will unavoidably result in a corresponding

```
unsigned char RC4NextKeySymbol(RC4_KEY *key) {
  unsigned char *d,x,y,tx,ty;

  x=key->x; y=key->y; d=key->data;
  x=((x+1)&0xff);     tx=d[x];
  y=(tx+y)&0xff;      d[x]=ty=d[y];
  d[y]=tx;  key->x=x; key->y=y;
  return d[(tx+ty)&0xff];
}

void RC4(RC4_KEY *key, const unsigned long len,
     const unsigned char *indata, unsigned char *outdata) {
  int i=0;
  while(i<len) { outdata[i]=indata[i] ^ RC4NextKeySymbol(key); i++; }
}
```

Figure 2: Simplified RC4 implementation

erroneous output symbol in the same position, will not affect subsequent outputs. Formally, following the notation introduced in Section 2 that associates V_H with the set of low-integrity input variables and V_L with the set of high-integrity outputs, we have for some $i \in [0, len[$:

$$V_H = \{\texttt{indata}[i]\}, \tag{6}$$

$$V_L = \{\texttt{outdata}[j] \,|\, i < j < \texttt{len}\}. \tag{7}$$

Verifying The Absence of Error Propagation in RC4. We are now in a position to apply self-composition to verify whether the RC4 implementation in `openSSL` indeed satisfies the error propagation property. To do this, we use the approach introduced in Section 4. As has been pointed out in literature [16], this is a non-trivial exercise even for such a small example: the self-composition technique for the verification of non-interference is very attractive from a conceptual point of view, but its applicability to real-world applications is yet to be demonstrated. We believe the results in this section are an important contribution towards answering this open question. However, given that we have used an off-the-shelf, general-purpose, verification tool, which is still under development, problems were to be expected. The limitations that we encountered were essentially due to the growth of the problem size due to the aggressive optimisations that were used in the RC4 implementation in `openSSL`: (1) the use of macros rather than (inline) function calls leads to a source code size expansion; (2) the use of loop unrolling leads to intricate control flow inside the function (namely the extensive use of the `break` statement); and (3) the use of pointer arithmetic greatly increases the complexity of the generated proof obligations. For this reason, a refactoring of the code was required in order to achieve the goals that we set out in the beginning of this section (more on this in Section 7). Figure 2 shows the version of the RC4 we used.

Appendix C contains the `Frama-c` input file used to show that the `RC4` function above does not introduce error propagation. The function is composed with itself and disjoint sets of variables are created for the two copies of the function, which is parametrised with the position i in which a transmission error could occur. The preconditions imposed on the composed function establish the equality of the high-integrity and unspecified-security input variables for both copies of the function: all input variables except position i in the `indata` buffers. The post-condition on the composed function requires that the high-integrity output variables have equal values upon termination: $i+1$ to `len` in the `outdata` buffer.

The verification of this code with `Frama-c` resulted in the generation of 17 proof obligations, all of

which are automatically discharged by the back-end prover `Simplify`. This is made possible by the inclusion of a helper lemma (proved offline with Coq following Section 4) in the ACSL annotations.

6 Related Work

Language Based Information Flow Security is surveyed in [14]. Leino and Joshi [11] were the first to propose a semantic approach to checking secure information flow, with several desirable features: it gives a more precise characterisation of security; it applies to all programming constructs whose semantics are well-defined; and it can be used to reason about indirect information leakage through variations in program behaviour (e.g., whether or not the program terminates). An attempt to capture this property in program logics using the *Java Modelling Language*(JML)[10] was presented by Warnier and Oostdijk[18]. They proposed an algorithm, based on the strongest post-condition calculus, that generates an annotated source file with specification patterns for confidentiality in JML. Dufay et al. [6] proposed an extension to JML to enforce non-interference through self-composition. This extended annotation language allows a simple definition of non-interference in Java programs. However, the generated proof obligations are complex, which limits the general applicability of the approach.

Terauchi and Aiken [16] identified problems in the self-composition approach, arguing that automatic tools are not powerful enough to verify this property over programs of realistic size. To compensate for this, the authors propose a program transformation technique to an extended version of self-composition approach, which incorporates the notion of security level downgrading using *relaxed non-interference* [12]. Rather than replicating the original code, the renamed version is interleaved and partially merged with it. Naumann[13] extended Terauchi and Aiken's work to encompass heap objects, presented a systematic method to validate the transformations proposed in [16], and reported on the experience of using these techniques with the ESC/JAVA2[9] and Spec# [1] tools.

7 Conclusions

The main contribution of this paper is to report on the application of the off-the-shelf `Frama-c` verification platform to a real-world example of a cryptographic software implementation: the widely used `C` implementation of the `RC4` stream cipher available in the `openSSL` library. Our results focus on two security-relevant properties of this implementation: (1) safety properties such as the absence of numeric errors and memory safety, and (2) the absence of error propagation.

We take advantage of the built-in `Frama-c` functionality that automatically generates proof obligations for these two common types of secure coding safety properties. Concretely, we use `Frama-c` to prove that `RC4` implementation does not cause null pointer de-referencing exceptions, and always performs array accesses with valid indices. In other words, the implementation is secure against *buffer overflow* attacks. Additionally, we demonstrate that the limited ranges of numeric variables used in the `RC4` implementation are guaranteed not to introduce calculation errors for particular input values.

An important property of stream ciphers such as `RC4` is their behaviour when a bit in the ciphertext is flipped over a communication channel. The change may be due to a transmission error, or maliciously introduced by an attacker. The behaviour of `RC4` is common to other *synchronous* ciphers: bit errors are not propagated in any way, i.e. if a ciphertext bit is flipped during transmission, then only the corresponding plaintext bit is affected. We formalise this property as a novel application of the *non-interference* concept, widely used in the formalisation and verification of secure information flow properties. Subsequently, we proved that the `RC4` implementation indeed has this property.

Finally, our work answers some open questions raised by previous work, which seemed to indicate that self-composition was not directly applicable to real-world cases. Our results are promising in that

we have been able to achieve our goal using an off-the-shelf verification tool and a technique with a high potential for automation. Our use of natural invariants is part of a bigger effort: the same technique facilitates the development of *equivalence proofs*, which we have been applying in the cryptographic context to prove the correctness of real implementations with respect to reference implementations. In particular, the validity of the refactoring used to produce the version of RC4 in Figure 2 can be addressed using these techniques. An extended version of this work covering also these aspects of our research is under preparation.

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A ACSL: Annotated openSSL implementation of RC4

```
typedef struct rc4_key_st
 {
 unsigned char x,y;
 unsigned char data[256];
 } RC4_KEY;

 /*@ lemma valid_range_ax1a:
   @ \forall unsigned long k; k>=0 ==> 0<=(k >> 3L)<=k;
   @
   @ lemma valid_range_ax1b:
   @ \forall unsigned long k; k>=0 ==> 0<=((k >> 3L)*8)<=k;
   @
   @ lemma valid_range_ax1c:
   @ \forall unsigned long k; k>=0 ==> 0<= (k & 0x07) <= 7;
   @
   @ lemma valid_range_ax1d:
   @ \forall unsigned long k; k>=0 ==> 0<= (k & 0x07) <= k;
   @
   @ lemma valid_range_ax1e:
   @ \forall unsigned long k; k>=0 ==> ((k >> 3L)*8 + (k & 0x07) == k);
   @
   @ lemma valid_range_ax1f:
   @ \forall unsigned long k; 0<= k < INT_MAX ==> 0<=(k >> 3L)< (INT_MAX / 8);
   @
   @ lemma valid_range_ax1g:
   @ \forall unsigned long k, unsigned long i; 0<= k < INT_MAX &&
   @  0<=i<=(k>>3L) ==> 0<=(((k >> 3L)-i)*8) < INT_MAX;
   @
   @ lemma valid_range_ax2a:
   @ \forall unsigned char k; 0 <= (k & 0xff) < 256;
   @
   @ lemma valid_range_ax2b:
   @ \forall unsigned char k; 0 <= k < 256;
   @
   @ lemma valid_range_ax2c:
   @ \forall unsigned char k; 0 <= ((k + 1) & 0xff) < 256;
   @
   @ lemma valid_range_ax2d:
   @ \forall unsigned char k, unsigned char l; 0 <= ((k + l) & 0xff) < 256;
   @
   @ lemma valid_range_ax4:
   @ \forall unsigned char inp; \forall unsigned char k;
   @ 0 <= (k ^ inp)<256;
   @*/

 /*@ requires len >= 0 &&
  @  \valid(key) &&
  @  \valid(key->data + (0..255) ) &&
  @  \valid(indata + (0..(len-1))) &&
  @  \valid(outdata + (0..(len-1)));
  @*/
void RC4(RC4_KEY *key,const unsigned long len,
```

```
       unsigned char *indata,
     unsigned char *outdata)
 {
         register unsigned char *d;
         register unsigned char x,y,tx,ty;
   int i;

         x=key->x;
         y=key->y;
         d=key->data;

#define LOOP(in,out) \
  x=((x+1)&0xff); \
  tx=d[x]; \
  y=((tx+y)&0xff); \
  d[x]=ty=d[y]; \
  d[y]=tx; \
  (out) = d[((tx+ty)&0xff)]^ (in);

#define RC4_LOOP(a,b,i) LOOP(a[i],b[i])

  i=(int)(len>>3L);

  /*@ ghost int old_i = i;*/
  /*@ ghost unsigned char *old_indata = indata;*/
  /*@ ghost unsigned char *old_outdata = outdata;*/


  /*@ ghost goto L;
    @*/
  /*@ ghost L:
    @*/
 if (i)
  {
  /*@ loop invariant (0 < i <= (len>>3L)) &&
    @  indata == old_indata + ((old_i - i)*8) &&
    @  outdata == old_outdata + ((old_i - i)*8);
     @ loop variant i;
       @*/
  while(1)
   {
   RC4_LOOP(indata,outdata,0);
   RC4_LOOP(indata,outdata,1);
   RC4_LOOP(indata,outdata,2);
   RC4_LOOP(indata,outdata,3);
   RC4_LOOP(indata,outdata,4);
   RC4_LOOP(indata,outdata,5);
   RC4_LOOP(indata,outdata,6);
   RC4_LOOP(indata,outdata,7);
   indata+=8;
   outdata+=8;
   if (--i == 0) break;
   }
  }
```

```
  /*@ assert i==0 &&
    @  indata == old_indata + ((len >> 3L)*8) &&
    @  outdata == old_outdata +((len >> 3L)*8);
    @*/

 i=(int)(len&0x07);

 if(i)
  {
  /*@ loop invariant i <=(len&0x07) && i>0;
     @ loop variant i;
       @*/
  while(1)
   {
   RC4_LOOP(indata,outdata,0); if (--i == 0) break;
   /*@ assert i>0;*/
   RC4_LOOP(indata,outdata,1); if (--i == 0) break;
   /*@ assert i>0;*/
   RC4_LOOP(indata,outdata,2); if (--i == 0) break;
   /*@ assert i>0;*/
   RC4_LOOP(indata,outdata,3); if (--i == 0) break;
   /*@ assert i>0;*/
   RC4_LOOP(indata,outdata,4); if (--i == 0) break;
   /*@ assert i>0;*/
   RC4_LOOP(indata,outdata,5); if (--i == 0) break;
   /*@ assert i>0;*/
   RC4_LOOP(indata,outdata,6); if (--i == 0) break;
   }
  }

 key->x=x;
 key->y=y;
}
```

B ACSL: Fibonacci Self-composition

```
/*@ inductive natinv(integer f1i, integer f2i, integer ni,
  @                integer f1o, integer f2o, integer no){
  @ case natinv_base :
  @   \forall integer f1, integer f2, integer n;
  @    natinv(f1, f2, n, f1, f2, n);
  @ case natinv_ind :
  @   \forall integer f1i, integer f2i, integer ni,
  @           integer f1t, integer f2t, integer nt;
  @    natinv(f1i, f2i, ni, f1t, f2t, nt) ==> nt>0 ==>
  @          natinv(f1i, f2i, ni, f1t + f2t, f1t, nt-1);
  @ }
  @ lemma natinv_unique :
  @   \forall integer f1i, integer f2i, integer n,
  @           integer f1o, integer f2o, integer no,
  @           integer f1so, integer f2so, integer nso;
  @    natinv(f1i, f2i, n, f1o, f2o, no)
  @    ==> natinv(f1i, f2i, n, f1so, f2so, nso)
  @    ==> no == nso
  @    ==> (f1o == f1so && f2o == f2so);
  @*/

/*@ requires n>=0 && ns>=0
  @      && l == ls && f1 == f1s && f2 == f2s && k == ks && n == ns;
  @ ensures l == ls;
  @*/
void fibonacci_verif ()
{
  /*@ loop invariant n>=0 &&
    @ natinv(\at(f1,Pre),  \at(f2,Pre),  \at(n,Pre),
    @        \at(f1,Here), \at(f2,Here), \at(n,Here));
    @*/
  while (n > 0) {
    f1 = f1 + f2; f2 = f1 - f2; n--;
  }
  if (f1 > k) l = 1;
  else l = 0;

  /*@ loop invariant ns>=0 &&
    @ natinv(\at(f1s,Pre), \at(f2s,Pre), \at(ns,Pre), f1s, f2s, ns);
    @*/
  while (ns > 0) {
    f1s = f1s + f2s; f2s = f1s - f2s; ns--;
  }
  if (f1s > ks) ls = 1;
  else ls = 0;
}
```

C ACSL: Error Propagation

```
typedef struct rc4_key_st
 {
 unsigned char x,y;
 unsigned char data[256];
 } RC4_KEY;

 /*@ // eqCondByK: u1 and u2 are equal in every positions differents from K
   @ predicate eqCondByK{L1,L2}(integer k,
   @      unsigned char u1[],unsigned char u2[]) =
   @  \forall integer l; l!=k ==> \at(u1[l],L1)==\at(u2[l],L2);
   @*/

 /*@ // eqArrays : u1 and u2 are equal in every positions between K and M
   @ predicate eqArrays{L1,L2}(
   @   unsigned char u1[],unsigned char u2[]) =
   @  \forall integer l; \at(u1[l],L1)==\at(u2[l],L2);
   @*/

 /*@ axiomatic RC4KeyAxiom {
   @ logic unsigned char RC4KeyLogic{L1,L2}(unsigned char *x, unsigned char *y,
   @                                        unsigned char *d);
   @ axiom RC4KeyLogic_unique{L1,L2,L3,L4}:
   @  \forall unsigned char *x, unsigned char *y,
   @     unsigned char *d, unsigned char *x1, unsigned char *y1,
   @     unsigned char *d1;
   @ \at(*x,L1)==\at(*x1,L3) ==> \at(*y,L1)==\at(*y1,L3)  ==>
   @ eqArrays{L1,L3}(d,d1) ==>
   @ (RC4KeyLogic{L1,L2}(x,y,d) == RC4KeyLogic{L3,L4}(x1,y1,d1) &&
   @ \at(*x,L2)==\at(*x1,L4) && \at(*y,L2)==\at(*y1,L4) &&
   @ eqArrays{L2,L4}(d,d1));
   @ }
   @*/

/*@ inductive spec1{L1,L2}(integer i1,integer i2,integer k, unsigned char *inp,
  @  unsigned char *out,unsigned char *x,
  @  unsigned char *y,unsigned char *d) {
  @ case spec1_base{L} :
  @  \forall integer i1,integer i2,integer k, unsigned char *inp,unsigned char *out,
  @    unsigned char *x, unsigned char *y, unsigned char *d;
  @  i1 == i2 ==> spec1{L,L}(i1,i2,k,inp,out,x,y,d);
  @
  @ case spec1_nat{L1,L2,L3} :
  @  \forall integer i1,integer i2,integer i3,integer k,
  @    unsigned char *inp, unsigned char *out,
  @    unsigned char *x, unsigned char *y,
  @    unsigned char *d;
  @  spec1{L1,L2}(i1,i2,k,inp,out,x,y,d) ==>
  @  i3 == i2 +1 ==>
  @  eqCondByK{L2,L3}(k,inp,inp) ==>
  @  \at(out[i2],L3) == (\at(inp[i2],L2) ^ RC4KeyLogic{L2,L3}(x,y,d)) ==>
  @  spec1{L1,L3}(i1,i3,k,inp,out,x,y,d);
  @ }
```

```
  @*/

/*@ lemma spec1_end{L1,L2,L3,L4} :
  @   \forall integer i,integer i1, integer i2,
  @    unsigned char *inp1,unsigned char *inp2,
  @    unsigned char *out1,unsigned char *out2,
  @    unsigned char *x1, unsigned char *x2,
  @    unsigned char *y1, unsigned char *y2,
  @       unsigned char *d1,unsigned char *d2,
  @    integer k;
  @  spec1{L1,L2}(i,i1,k,inp1,out1,x1,y1,d1) ==>
  @  spec1{L3,L4}(i,i2,k,inp2,out2,x2,y2,d2) ==>
  @  eqCondByK{L1,L3}(k,inp1,inp2) ==>
  @  eqArrays{L1,L3}(d1,d2) ==>
  @  \at(*x1,L1)==\at(*x2,L3) ==>
  @  \at(*y1,L1)== \at(*y2,L3) ==>
  @  i1 == i2 ==>
  @  eqCondByK{L2,L4}(out1,out2);
  @*/

/*@ ensures \result == RC4KeyLogic{Old,Here}(&(key->x),&(key->y),key->data);
  @*/
unsigned char RC4NextKeySymbol(RC4_KEY *key) {
 register unsigned char *d;
 register unsigned char x,y,tx,ty;
 x=key->x; y=key->y; d=key->data;
 x=((x+1)&0xff); tx=d[x];
 y=(tx+y)&0xff; d[x]=ty=d[y];
 d[y]=tx; key->x = x; key->y=y;
 return d[(tx+ty)&0xff];
}

/*@ requires len>=0 && len1>=0 && len==len1 &&
  @     eqCondByK{Here,Here}(k,indata,indata1) &&
  @     eqArrays{Here,Here}(key->data,key1->data) &&
  @  key->x==key1->x && key->y==key1->y;
  @ ensures (\forall int l; k < l < len ==>
  @    \at(outdata[l],Here)==\at(outdata1[l],Here));
  @*/
void RC4_SC(RC4_KEY *key, const unsigned long len,
 const unsigned char *indata, unsigned char *outdata,
 RC4_KEY *key1, const unsigned long len1,
 const unsigned char *indata1, unsigned char *outdata1,
 int k) {
 int i=0;
 int i1=0;

 /*@ loop invariant 0<=i<=len &&
   @  spec1{Pre,Here}(0,i,k,indata,outdata,&(key->x),&(key->y),key->data);
   @ loop variant (len-i);
   @*/
 while(i<len) {
 outdata[i]=indata[i] ^ RC4NextKeySymbol(key);
 i++;
```

```
 }

 /*@ loop invariant 0<=i1<=len1 &&
   @  spec1{Pre,Here}(0,i1,k,indata1,outdata1,&(key1->x),&(key1->y),key1->data);
   @ loop variant (len1-i1);
   @*/
 while(i1<len1) {
 outdata1[i1]=indata1[i1] ^ RC4NextKeySymbol(key1);
 i1++;
 }
}
```